

# MAX32655 ERRATA SHEET

## Revision A1 Errata

The errata listed below describe situations where components of this revision perform differently than expected or differently than described in the data sheet. Maxim Integrated Products, Inc. may, at its own discretion, take future steps to correct these errata when the opportunity to redesign the product presents itself. Prior to that, Maxim has determined the following potential workarounds that customers may want to consider when addressing one of the situations described below.

This errata sheet only applies to components of this revision. These components are branded on the top side of the package with a six-digit code in the form yywwRR, where yy and ww are two-digit numbers representing the year and work week of manufacture, respectively, and RR is the revision of the component. To obtain an errata sheet on other die revisions, visit the Maxim website at [www.maximintegrated.com/errata](http://www.maximintegrated.com/errata).

### 1) BOOTLOADER SHA-256 HASH CHECK COMMAND OUTPUTS INCORRECT RESULT WHEN LENGTH NOT A MULTIPLE OF 64 BYTES

**Description:**

Bootloader SHA-256 Hash Check Command "H" outputs incorrect result when length is not a multiple of 64 bytes.

**Workaround:**

When issuing the "H" command, ensure that the requested HASH length is a multiple of 64 bytes.

### 2) HIGH $V_{REG1}$ SUPPLY CURRENT IN BACKUP (BKU) AND STANDBY (STB) MODES OF OPERATION

**Description:**

$V_{REG1}$  supply current is higher than expected in BACKUP (BKU) and STANDBY (STB) modes of operation.

**Workaround:**

When it is desired to operate in BACKUP or STANDBY modes of operation, the  $V_{DDA}$  power supply pin should be biased to its lowest range of operation. This will minimize  $V_{REG1}$  current. Refer to the device datasheet for  $V_{DDA}$  power supply pin operating range.

### 3) SPI MODE 1 AND MODE 3 FAIL AT MAXIMUM SERIAL CLOCK RATES

**Description:**

Both SPI0 and SPI1 include this limitation in both MASTER and SLAVE mode operation.

**Workaround:**

Operate the SPI0 at a maximum speed of 25MHz for both MASTER and SLAVE mode. Operate SPI1 at a maximum speed of 25MHz for MASTER mode and 12.5MHz for SLAVE mode.

# MAX32655

## REV A1 ERRATA

### 4) USE OF WELR/RLR REGISTERS CAUSES EXECUTION HALT ON SUBSEQUENT RESETS

**Description:**

The WELR/RLR register is not reset on all forms of reset. It is only reset during a power-on cycle (POR).

Any use of WELR/RLR registers by user code will cause an execution halt on all subsequent reset events other than POR.

**Workarounds:**

Do not write WELR/RLR registers in user code.

If this workaround is required, the user must power cycle the device to avoid execution halt.

### 5) ALL ADC/COMPARATOR INPUTS HAVE RESTRICTED OPERATING VOLTAGE RANGE.

**Description:**

The operating range of the ADC/Comparator inputs is restricted to maximum 1.8V.

**Workaround:**

None.

### 6) DEVICE WILL NOT EXIT FROM MICRO POWER MODE WHILE RUNNING FROM THE IBRO.

**Description:**

If the device is running from the IBRO and then subsequently enters the MICRO POWER mode, the device will not exit properly from the MICRO POWER mode as intended.

**Workaround:**

Set the System Clock ( $f_{\text{SYS\_CLK}}$ ) to operate from the IPO or the ISO before entering MICRO POWER Mode.

# MAX32655

## REV A1 ERRATA

### Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	08/2020	Initial release	—

*Maxim Integrated cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim Integrated product. No circuit patent licenses are implied. Maxim Integrated reserves the right to change the circuitry and specifications without notice at any time.*

**Maxim Integrated 160 Rio Robles, San Jose, CA 95134 USA 1-408-601-1000**

© 2020 Maxim Integrated Products, Inc. Maxim Integrated and the Maxim Integrated logo are trademarks of Maxim Integrated Products, Inc.