



## ERRATA SHEET DS26502

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### Revision C1 Errata

The errata listed below describe situations where DS26502 revision C1 components perform differently than expected or differently than described in the data sheet. Maxim Integrated Products, Inc., intends to correct these errata when the opportunity to redesign the product presents itself.

This errata sheet only applies to DS26502 revision C1 components. Revision C1 components are branded on the topside of the package with a six-digit code in the form yywwC1, where yy and ww are two-digit numbers representing the year and work week of manufacture, respectively. To obtain an errata sheet on another DS26502 die revision, visit our website at [www.maxim-ic.com/errata](http://www.maxim-ic.com/errata).

#### 1) TS\_8K\_4 PIN JTAG FUNCTIONALITY DOES NOT WORK PROPERLY IN TRANSMIT 6312kHz HARDWARE MODE

**Description:**

When the part is configured for transmit 6312kHz hardware mode, the TS\_8K\_4 pin will always sample a logic 1 in JTAG mode. This pin is not used in the transmit 6312kHz operating mode.

**Workaround:**

None.