General Description
The Himalaya series of voltage regulator ICs, power modules, and chargers enable cooler, smaller, and simpler power supply solutions. The MAXM17505 is an easy-to-use, Himalaya step-down power module that combines a switching power supply controller, dual n-channel MOSFET power switches, fully shielded inductor, and the compensation components in a low-profile, thermally-efficient, system-in-package (SiP). The device operates over a wide input voltage range of 4.5V to 60V and delivers up to 1.7A continuous output current with excellent line and load regulation over an output voltage range of 0.9V to 12V. The device only requires five external components to complete the total power solution. The high level of integration significantly reduces design complexity, manufacturing risks, and offers a true plug-and-play power supply solution, reducing time-to-market.

The device can be operated in the pulse-width modulation (PWM), pulse-frequency modulation (PFM), or discontinuous conduction mode (DCM) control schemes.

The MAXM17505 is available in a low-profile, highly thermal-emissive, compact, 29-pin 9mm x 15mm x 2.8mm SiP package that reduces power dissipation in the package and enhances efficiency. The package is easily soldered onto a printed circuit board and suitable for automated circuit board assembly. The device can operate over a wide industrial temperature range from -40°C to +125°C.

Applications
- Industrial Power Supplies
- Distributed Supply Regulation
- FPGA and DSP Point-of-Load Regulator
- Base Station Point-of-Load Regulator
- HVAC and Building Control

Benefits and Features
- Saves Board Space in Space-Constrained Applications
  - Complete Integrated Step-Down Power Supply in a Single Package
  - Small Profile 9mm x 15mm x 2.8mm SiP Package
  - Simplified PCB Design with Minimal External BOM Components
- Offers Flexibility for Power-Design Optimization
  - Wide Input Voltage Range from 4.5V to 60V
  - Output-Voltage Adjustable Range from 0.9V to 12V
  - Adjustable Frequency with External Frequency Synchronization (100kHz to 1.8MHz)
  - Soft-Start Programmable
  - PWM, PFM, or DCM Current-Mode Control
  - Optional Programmable EN/UVLO
- Operates Reliably in Adverse Industrial Environments
  - Integrated Thermal Fault Protection
  - Hiccup Mode Overload Protection
  - RESET/Output-Voltage Monitoring
  - Wide Industrial Ambient Operating Temperature Range (-40°C to +125°C)/Junction Temperature Range (-40°C to +150°C)
  - Complies with CISPR22(EN55022) Class B Conducted and Radiated Emissions

Ordering Information appears at end of data sheet.
MAXM17505 4.5V to 60V, 1.7A High-Efficiency, DC-DC Step-Down Power Module with Integrated Inductor

Absolute Maximum Ratings  (Notes 1, 2)

- IN to PGND (Note 2) ........................................... -0.3V to +65V
- EN to SGND (Note 2) ........................................... -0.3V to +65V
- VCC .............................................................. -0.3V to min (VIN + 0.3V, 6.5V)
- BST to PGND .................................................. -0.3V to +70V
- BST to VCC ...................................................... -0.3V to +65V
- BST to LX ........................................................ -0.3V to +6.5V
- FB, RESET, SS, CF, MODE, SYNC, RT to SGND ........... -0.3V to +6.5V
- OUT to PGND (VIN < 25V) .................................. -0.3V to (VIN + 0.3V)
- OUT to PGND (VIN ≥ 25V) .................................. -0.3V to +25V
- LX to PGND ........................................................ -0.3V to (VIN + 0.3V)
- Operating Temperature Range ..................... -40°C to +125°C
- Junction Temperature ........................................... +125°C
- Storage Temperature Range ..................... -65°C to +125°C
- Lead Temperature (soldering, 10s) .................. +245°C

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Package Information

<table>
<thead>
<tr>
<th>Package Code</th>
<th>L32915+1</th>
</tr>
</thead>
<tbody>
<tr>
<td>Outline Number</td>
<td>21-0879</td>
</tr>
<tr>
<td>Land Pattern Number</td>
<td>90-0459</td>
</tr>
<tr>
<td>THERMAL RESISTANCE (Note 3)</td>
<td>30.8°C/W</td>
</tr>
</tbody>
</table>

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a “+”, “#”, or “-” in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

**Note 1:** SGND and PGND are internally connected.
**Note 2:** See *Pin Description* for the connection of the backside exposed pad.
**Note 3:** Data taken using Maxim's evaluation kit, MAXM17505EVKIT#.
## Electrical Characteristics

\( V_{IN} = V_{EN} = 24V, R_{RT} = 40.2k\Omega \) (500kHz) to SGND, \( V_{PGND} = V_{MODE} = V_{SYNC} = V_{SGND} = 0V, V_{CC} = LX = SS = RESET = OUT = open, V_{BST} \) to \( V_{LX} = 5V, V_{FB} = 1V, T_A = T_J = -40°C \) to +125°C, unless otherwise noted. Typical values are at \( T_A = +25°C \). All voltages are referenced to SGND, unless otherwise noted.) (Note 4)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SYMBOL</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>INPUT SUPPLY ((V_{IN}))</td>
<td>( V_{IN} )</td>
<td></td>
<td>4.5</td>
<td>60</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>IN Input Voltage Range</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input Shutdown Current</td>
<td>( I_{IN_SH} )</td>
<td>( V_{EN} = 0V )</td>
<td>10.5</td>
<td>13</td>
<td></td>
<td>( \mu A )</td>
</tr>
<tr>
<td>Input Quiescent Current</td>
<td>( I_{P_Q_HIB} )</td>
<td>MODE = ( RT = ) open</td>
<td>125</td>
<td></td>
<td></td>
<td>( \mu A )</td>
</tr>
<tr>
<td></td>
<td>( I_{Q_DCM} )</td>
<td>MODE = ( V_{CC} )</td>
<td>1.16</td>
<td>1.8</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td>( I_{Q_PWM} )</td>
<td>Normal switching mode, no load</td>
<td>9.5</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>LOGIC INPUTS</td>
<td>( V_{ENR} )</td>
<td>( V_{EN} ) rising</td>
<td>1.192</td>
<td>1.215</td>
<td>1.26</td>
<td>V</td>
</tr>
<tr>
<td>EN Threshold</td>
<td>( V_{ENF} )</td>
<td>( V_{EN} ) falling</td>
<td>1.068</td>
<td>1.09</td>
<td>1.131</td>
<td>V</td>
</tr>
<tr>
<td>Enable Pullup Resistor</td>
<td>( R_{ENP} )</td>
<td>Pullup resistor between IN and EN pins</td>
<td>3.15</td>
<td>3.3</td>
<td>3.45</td>
<td>M( \Omega )</td>
</tr>
<tr>
<td>LDO</td>
<td>( V_{CC} )</td>
<td>( 6V &lt; V_{IN} &lt; 60V, 1mA &lt; I_{VCC} &lt; 25mA )</td>
<td>4.75</td>
<td>5</td>
<td>5.25</td>
<td>V</td>
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<tr>
<td>( V_{CC} ) Output Voltage Range</td>
<td>( I_{VCC_MAX} )</td>
<td>( V_{IN} = 6V, V_{CC} = 4.3V )</td>
<td>26.5</td>
<td>60</td>
<td>100</td>
<td>mA</td>
</tr>
<tr>
<td>( V_{CC} ) Current Limit</td>
<td>( V_{CC_DO} )</td>
<td>( V_{IN} = 4.5V, I_{VCC} = 20mA )</td>
<td>4.2</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>( V_{CC} ) Dropout</td>
<td>( V_{CC_UVR} )</td>
<td>( V_{CC} ) rising</td>
<td>4.05</td>
<td>4.2</td>
<td>4.3</td>
<td>V</td>
</tr>
<tr>
<td>( V_{CC} ) UVLO</td>
<td>( V_{CC_UVR} )</td>
<td>( V_{CC} ) falling</td>
<td>3.65</td>
<td>3.8</td>
<td>3.9</td>
<td>V</td>
</tr>
<tr>
<td>OUTPUT SPECIFICATIONS</td>
<td>( V_{FB_REG} )</td>
<td>MODE = SGND</td>
<td>0.887</td>
<td>0.910</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>FB Regulation Voltage</td>
<td></td>
<td>MODE = open</td>
<td>0.890</td>
<td>0.915</td>
<td>0.936</td>
<td></td>
</tr>
<tr>
<td>FB Input Bias Current</td>
<td>( I_{FB} )</td>
<td>( 0V &lt; V_{FB} &lt; 1V, T_A = +25°C )</td>
<td>-50</td>
<td></td>
<td>+50</td>
<td>nA</td>
</tr>
<tr>
<td>FB Undervoltage Trip Level to Cause Hiccup</td>
<td>( V_{FB_HICF} )</td>
<td></td>
<td>0.56</td>
<td>0.58</td>
<td>0.65</td>
<td>V</td>
</tr>
<tr>
<td>Hiccup Timeout</td>
<td></td>
<td></td>
<td>32,768</td>
<td></td>
<td></td>
<td>Cycles</td>
</tr>
<tr>
<td>SOFT-START (SS)</td>
<td>( I_{SS} )</td>
<td>( V_{SS} = 0.5V )</td>
<td>4.7</td>
<td>5</td>
<td>5.3</td>
<td>( \mu A )</td>
</tr>
</tbody>
</table>
Electrical Characteristics

\( \text{V}_{\text{IN}} = \text{V}_{\text{EN}} = 24\text{V}, \text{R}_{\text{RT}} = 40.2\text{k}\Omega \text{ (500kHz)} \) to SGND, \( \text{VP}_{\text{GND}} = \text{V}_{\text{MODE}} = \text{V}_{\text{SYNC}} = \text{V}_{\text{SGND}} = 0\text{V}, \text{V}_{\text{CC}} = \text{LX} = \text{SS} = \text{RESET} = \text{OUT} = \text{open}, \text{V}_{\text{BST}} \) to \( \text{V}_{\text{LX}} = 5\text{V}, \text{V}_{\text{FB}} = 1\text{V}, \text{T}_{\text{A}} = \text{T}_{\text{J}} = -40^\circ\text{C} \text{ to } +125^\circ\text{C}, \) unless otherwise noted. Typical values are at \( \text{T}_{\text{A}} = +25^\circ\text{C}. \) All voltages are referenced to SGND, unless otherwise noted.) (Note 4)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SYMBOL</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>RT AND SYNC</strong></td>
<td></td>
<td></td>
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<tr>
<td>Switching Frequency</td>
<td>( f_{\text{SW}} )</td>
<td>( \text{R}_{\text{RT}} = 210\text{k}\Omega )</td>
<td>90</td>
<td>100</td>
<td>110</td>
<td>kHz</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( \text{R}_{\text{RT}} = 9.76\text{k}\Omega )</td>
<td></td>
<td></td>
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</tr>
<tr>
<td></td>
<td></td>
<td>( \text{R}_{\text{RT}} = \text{open} )</td>
<td>450</td>
<td>500</td>
<td>550</td>
<td>kHz</td>
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<tr>
<td>SYNC Frequency Range</td>
<td></td>
<td>1.1 x ( f_{\text{SW}} )</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>1.4 x ( f_{\text{SW}} )</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SYNC Pulse Width</td>
<td>( V_{\text{IH}} )</td>
<td></td>
<td>50</td>
<td></td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td></td>
<td>( V_{\text{IL}} )</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td><strong>MODE</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MODE Threshold</td>
<td>( V_{\text{M, DCM}} )</td>
<td>( \text{MODE} = \text{V}_{\text{CC}} ) (DCM mode)</td>
<td>( \text{V}_{\text{CC}} - 1.6 )</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>( V_{\text{M, PFM}} )</td>
<td>( \text{MODE} = \text{open} ) (PFM mode)</td>
<td>( \text{V}_{\text{CC}} / 2 )</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>( V_{\text{M, PWM}} )</td>
<td>( \text{MODE} = \text{GND} ) (PWM mode)</td>
<td>1.4</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>CURRENT LIMIT</strong></td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Average Current-Limit Threshold</td>
<td>( I_{\text{AVG LIMIT}} )</td>
<td>( \text{V}<em>{\text{OUT}} = \text{V}</em>{\text{FB}} = 0.8\text{V}, f_{\text{SW}} = 200\text{kHz} )</td>
<td>2.4</td>
<td></td>
<td></td>
<td>A</td>
</tr>
<tr>
<td><strong>RESET</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>RESET Output Level Low</td>
<td>( I_{\text{RESET}} )</td>
<td></td>
<td>10mA</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>RESET Output Leakage Current</td>
<td></td>
<td></td>
<td></td>
<td>-0.1</td>
<td>+0.1</td>
<td>( \mu\text{A} )</td>
</tr>
<tr>
<td>FB Threshold for RESET Assertion</td>
<td>( \text{V}_{\text{FB_OKF}} )</td>
<td>( \text{V}_{\text{FB}} ) falling</td>
<td>90.5</td>
<td>92</td>
<td>94.6</td>
<td>%</td>
</tr>
<tr>
<td>FB Threshold for RESET Deassertion</td>
<td>( \text{V}_{\text{FB_OKR}} )</td>
<td>( \text{V}_{\text{FB}} ) rising</td>
<td>93.8</td>
<td>95</td>
<td>97.8</td>
<td>%</td>
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<tr>
<td>RESET Deassertion Delay After FB Reaches 95% Regulation</td>
<td></td>
<td></td>
<td>1024</td>
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<td></td>
<td>Cycles</td>
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<tr>
<td><strong>THERMAL SHUTDOWN</strong></td>
<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td>Thermal-Shutdown Threshold</td>
<td></td>
<td>Temperature rising</td>
<td></td>
<td>+165</td>
<td></td>
<td>°C</td>
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<tr>
<td>Thermal-Shutdown Hysteresis</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>°C</td>
</tr>
</tbody>
</table>

**Note 4:** All limits are 100% tested at \( \text{T}_{\text{A}} = +25^\circ\text{C}. \) Maximum and minimum limits are guaranteed by design and characterized over temperature.
Typical Operating Characteristics

\( V_{\text{IN}} = 4.5\text{V to } 60\text{V}, V_{\text{OUT}} = 0.9\text{V to } 12\text{V}, I_{\text{OUT}} = 0\text{A to } 1.7\text{A}, T_{\text{A}} = +25^\circ\text{C}, \text{ unless otherwise noted.} \)
Typical Operating Characteristics (continued)

(VIN = 4.5V to 60V, VOUT = 0.9V to 12V, IOUT = 0A to 1.7A, TA = +25°C, unless otherwise noted.)

---

**EFFICIENCY vs. OUTPUT CURRENT**

*VOUT = 1.2V, PFM MODE*

*MODE = OPEN*

*VIN = 5V, fSW = 350kHz*

*VIN = 12V, fSW = 350kHz*

*VIN = 24V, fSW = 285kHz*

*VIN = 36V, fSW = 200kHz*

---

**EFFICIENCY vs. OUTPUT CURRENT**

*VOUT = 0.9V, PWM MODE*

*MODE = SGND*

*VIN = 5V, fSW = 300kHz*

*VIN = 12V, fSW = 300kHz*

*VIN = 24V, fSW = 200kHz*

*VIN = 36V, fSW = 200kHz*

---

**LOAD REGULATION**

*VOUT = 3.3V, PFM MODE*

*MODE = OPEN*

*VIN = 5V, fSW = 400kHz*

*VIN = 12V, fSW = 400kHz*

*VIN = 24V, fSW = 400kHz*

*VIN = 48V, fSW = 360kHz*

*VIN = 36V, fSW = 400kHz*

---

**Load Regulation**

*VOUT = 5V, PWM MODE*

*MODE = SGND*

*VIN = 5V, fSW = 300kHz*

*VIN = 12V, fSW = 300kHz*

*VIN = 24V, fSW = 200kHz*

*VIN = 36V, fSW = 200kHz*

---

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MAXM17505 4.5V to 60V, 1.7A High-Efficiency, DC-DC Step-Down Power Module with Integrated Inductor

Typical Operating Characteristics (continued)

(V_{\text{IN}} = 4.5V to 60V, V_{\text{OUT}} = 0.9V to 12V, I_{\text{OUT}} = 0A to 1.7A, T_{A} = +25°C, unless otherwise noted.)
Typical Operating Characteristics (continued)

(V_{IN} = 4.5V to 60V, V_{OUT} = 0.9V to 12V, I_{OUT} = 0A to 1.7A, T_{A} = +25^\circ C, unless otherwise noted.)

LOAD CURRENT TRANSIENT RESPONSE
V_{IN} = 24V, V_{OUT} = 3.3V, I_{OUT} = 0.05A - 0.85A,
MODE = V_{CC}

LOAD CURRENT TRANSIENT RESPONSE
V_{IN} = 24V, V_{OUT} = 5V, I_{OUT} = 0.05A - 0.85A,
MODE = OPEN

LOAD CURRENT TRANSIENT RESPONSE
V_{IN} = 24V, V_{OUT} = 5V, I_{OUT} = 0.05A - 0.85A,
MODE = SGND

LOAD CURRENT TRANSIENT RESPONSE
V_{IN} = 24V, V_{OUT} = 3.3V, I_{OUT} = 0A,
MODE = SGND

STARTUP THROUGH ENABLE
V_{IN} = 24V, V_{OUT} = 3.3V, I_{OUT} = 0A, MODE = SGND

STARTUP WITH 2.5V PREBIAS
V_{IN} = 24V, V_{OUT} = 3.3V, I_{OUT} = 0A, MODE = SGND

SHUTDOWN THROUGH ENABLE
V_{IN} = 24V, V_{OUT} = 3.3V, I_{OUT} = 0A, MODE = SGND
Typical Operating Characteristics (continued)

\( V_{IN} = 4.5V \) to 60V, \( V_{OUT} = 0.9V \) to 12V, \( I_{OUT} = 0A \) to 1.7A, \( T_{A} = +25°C \), unless otherwise noted.)

**STARTUP THROUGH INPUT SUPPLY**

\( V_{IN} = 24V, V_{OUT} = 3.3V, I_{OUT} = 1.7A, \text{ MODE } = \text{ SGND} \)

**SHUTDOWN THROUGH INPUT SUPPLY**

\( V_{IN} = 24V, V_{OUT} = 3.3V, I_{OUT} = 1.7A, \text{ MODE } = \text{ SGND} \)

**STARTUP THROUGH ENABLE**

\( V_{IN} = 24V, V_{OUT} = 5V, I_{OUT} = 0A, \text{ MODE } = \text{ SGND} \)

**SHUTDOWN THROUGH ENABLE**

\( V_{IN} = 24V, V_{OUT} = 5V, I_{OUT} = 0A, \text{ MODE } = \text{ SGND} \)
Typical Operating Characteristics (continued)

(V\text{IN} = 4.5\text{V} to 60\text{V}, V\text{OUT} = 0.9\text{V} to 12\text{V}, I\text{OUT} = 0\text{A} to 1.7\text{A}, T_A = +25^\circ\text{C}, unless otherwise noted.)

- **OUTPUT SHORT IN STEADY STATE**
  - V\text{IN} = 24\text{V}, V\text{OUT} = 3.3\text{V}, I\text{OUT} = 0\text{A} to SHORT MODE = SGND

- **OUTPUT SHORT DURING STARTUP**
  - V\text{IN} = 24\text{V}, V\text{OUT} = 3.3\text{V}, I\text{OUT} = SHORT, MODE = SGND

- **SYNC FREQUENCY AT 740 KHZ**
  - V\text{IN} = 24\text{V}, V\text{OUT} = 5\text{V}, I\text{OUT} = 6\text{A}, MODE = GND

- **CLOSED-LOOP BODE PLOT**
  - V\text{IN} = 24\text{V}, V\text{OUT} = 3.3\text{V}, I\text{OUT} = 1.7\text{A}, MODE = SGND

- **OUTPUT CURRENT VS. AMBIENT TEMPERATURE**
  - V\text{IN} = 24\text{V} NO AIR FLOW

- **RADIATED EMISSION PLOT**
  - C4 = C14 = OPEN, L1 = SHORT
  - C7 = 220pF, C17 = C18 = 150pF

- **CISPR-22 CLASS B QP LIMIT**
  - V\text{OUT} = 3.3\text{V}
  - V\text{OUT} = 5\text{V}
  - V\text{OUT} = 12\text{V}

- **CONDUCTED EMISSION PLOT**
  - WITH FILTER: C4 = 2.2\mu\text{F}, L1 = 10\mu\text{H}, C14 = 2.2\mu\text{F}

- **OUTPUT CURRENT**

- **FREQUENCY (MHz)**
  - FROM MAXM17505EVBTKAE#
Pin Configuration

MAXM17505 4.5V to 60V, 1.7A High-Efficiency, DC-DC Step-Down Power Module with Integrated Inductor

www.maximintegrated.com

Maxim Integrated
### Pin Description

<table>
<thead>
<tr>
<th>PIN</th>
<th>NAME</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>1, 7</td>
<td>N.C.</td>
<td>No Connection</td>
</tr>
<tr>
<td>2</td>
<td>SYNC</td>
<td>Frequency Synchronization. The device can be synchronized to an external clock using this pin. See the <strong>External Frequency Synchronization</strong> section for more details.</td>
</tr>
<tr>
<td>3</td>
<td>SS</td>
<td>Soft-Start Input. Connect a capacitor from SS to SGND to set the soft-start.</td>
</tr>
<tr>
<td>4</td>
<td>CF</td>
<td>Compensation Filter. Connect capacitor from CF to FB to correct frequency with switching frequency below 500kHz. Leave CF open otherwise.</td>
</tr>
<tr>
<td>5</td>
<td>FB</td>
<td>Feedback Input. Connect FB to the center tap of an external resistor-divider from the OUT to SGND to set the output voltage. See the <strong>Adjusting Output Voltage</strong> section for more details.</td>
</tr>
<tr>
<td>6</td>
<td>RT</td>
<td>Frequency Set. Connect a resistor from RT to SGND to set the regulator’s switching frequency. Leave RT open for the default 500kHz frequency.</td>
</tr>
<tr>
<td>8</td>
<td>MODE</td>
<td>Light-Load Mode Selection. The MODE pin configures the MAXM17505 to operate in PWM, PFM, or DCM mode of operation. Leave MODE unconnected for PFM operation (pulse skipping at light loads). Connect MODE to SGND for constant-frequency PWM operation at all loads. Connect MODE to <strong>V&lt;sub&gt;CC&lt;/sub&gt;</strong> for DCM operation. See the <strong>MODE Setting</strong> section for more details.</td>
</tr>
<tr>
<td>9</td>
<td><strong>V&lt;sub&gt;CC&lt;/sub&gt;</strong></td>
<td>5V LDO Output. No external connection.</td>
</tr>
<tr>
<td>10</td>
<td>SGND</td>
<td>Analog Ground. Internally-shorted to PGND. Connect it to PGND through a single point at output capacitor.</td>
</tr>
<tr>
<td>11, 26</td>
<td>PGND</td>
<td>Power Ground. Connect the PGND pins externally to the power ground plane.</td>
</tr>
<tr>
<td>12–18</td>
<td>OUT</td>
<td>Regulator Output Pin. Connect a capacitor from OUT to PGND. See the <strong>PCB Layout Guidelines</strong> section for more connection details.</td>
</tr>
<tr>
<td>19–24</td>
<td>LX</td>
<td>Internally Connected to EP2. Do not connect these pins to external components for any reason.</td>
</tr>
<tr>
<td>25</td>
<td>BST</td>
<td>Boost Flying Cap Node. No external connection.</td>
</tr>
<tr>
<td>27</td>
<td>IN</td>
<td>Input Supply Connection. Bypass to PGND with a capacitor; place the capacitor close to the IN and PGND pins. See Table 1 for more details.</td>
</tr>
<tr>
<td>28</td>
<td><strong>EN</strong></td>
<td>Enable/Undervoltage-Lockout Input. Default enable through the pullup 3.3MΩ resistor between <strong>EN</strong> and IN. Connect a resistor from <strong>EN</strong> to SGND to set the UVLO threshold. If the <strong>EN/UVLO</strong> pin is driven by an external signal, a 50Ω damping resistor in series with the signal line driving <strong>EN/UVLO</strong> is required.</td>
</tr>
<tr>
<td>29</td>
<td><strong>RESET</strong></td>
<td>Open-Drain <strong>RESET</strong> Output. The <strong>RESET</strong> output is driven low if FB drops below 92% of its set value. <strong>RESET</strong> goes high 1024 clock cycles after FB rises above 95% of its set value.</td>
</tr>
<tr>
<td>EP1</td>
<td>SGND</td>
<td>Analog Ground. Connect this pad to 1in x 1in copper island with a lot of vias for cooling.</td>
</tr>
<tr>
<td>EP2</td>
<td>LX</td>
<td>Switching Node. Connect this pad to a small copper area of 1in x 1in under the device for thermal relief.</td>
</tr>
<tr>
<td>EP3</td>
<td>OUT</td>
<td>Connect this pad to the OUT pins and copper area of 1in x 1in.</td>
</tr>
</tbody>
</table>
MAXM17505 4.5V to 60V, 1.7A High-Efficiency, DC-DC Step-Down Power Module with Integrated Inductor

Functional Diagram
Design Procedure

Setting the Output Voltage

The MAXM17505 supports an adjustable output voltage range of 0.9V to 12V from an input voltage range of 4.5V to 60V by using a resistive feedback divider from OUT to FB. Table 1 provides the feedback dividers for desired input and output voltages. Other adjustable output voltages can be calculated by following the procedure to choose the resistive voltage-divider values:

Calculate resistor $R_U$ from the output to FB as follows:

$$R_U = \frac{216 \times 1000}{f_C \times C_{OUT}}$$

where $R_U$ is in kΩ, crossover frequency ($f_C$) is in kHz, and output capacitor ($C_{OUT}$) is in μF. Choose $f_C$ to be 1/9th of the switching frequency ($f_{SW}$) if the switching frequency is less than or equal to 500kHz. If the switching frequency is more than 500kHz, select $f_C$ to be 55kHz.

$$R_B = \frac{R_U \times 0.9}{V_{OUT} - 0.9}$$

where $R_B$ is in kΩ.

Input Voltage Range

The minimum and maximum operating input voltages for a given output voltage should be calculated as follows:

$$V_{IN(MIN)} = \frac{V_{OUT} + (I_{OUT(MAX)} \times 0.251)}{1 - (f_{SW(max)} \times t_{OFF_MIN(MAX)})} + (I_{OUT(MAX)} \times 0.175)$$

For $D > 0.4$, $V_{IN(MIN)} = 4.26 \times V_{OUT} - \frac{f_{SW}}{36600}$

$$V_{IN(MAX)} = \frac{f_{SW(max)} \times t_{OFF_MIN(MAX)}}{V_{OUT}}$$

where,

$V_{OUT}$ = Steady-state output voltage

$I_{OUT(MAX)}$ = Maximum load current

$f_{SW(max)}$ = Operating maximum switching frequency in Hz

$t_{OFF_MIN(MAX)}$ = Worst-case minimum switch off-time (160ns)

$t_{ON_MIN(MAX)}$ = Worst-case minimum switch on-time (80ns)

Input Capacitor Selection

The input capacitor serves to reduce the current peaks drawn from the input power supply and reduces switching noise to the IC. The input capacitor values in Table 1 are the minimum recommended values for desired input and output voltages. Applying capacitor values larger than those indicated in Table 1 are acceptable to improve the dynamic response. For further operating conditions, the total input capacitance must be greater than or equal to the value given by the following equation in order to keep the input-voltage ripple within specifications and minimize the high-frequency ripple current being fed back to the input source:

$$C_{IN} = \frac{(I_{IN_AVG}) \times (1 - D)}{(\Delta V_{IN}) \times f_{SW}}$$

where:

$I_{IN_AVG}$ is the average input current given by:

$$I_{IN_AVG} = \frac{P_{OUT}}{\eta \times V_{IN}}$$

$D$ is the operating duty cycle, which is approximately equal to $V_{OUT}/V_{IN}$.

$\Delta V_{IN}$ is the required input voltage ripple.

$f_{SW}$ is the operating switching frequency.

$P_{OUT}$ is the out power, which is equal to $V_{OUT} \times I_{OUT}$.

$\eta$ is the efficiency.

The input capacitor must meet the ripple-current requirement imposed by the switching currents. The RMS input ripple current is given by:

$$I_{RMS} = I_{OUT} \times \sqrt{D \times (1 - D)}$$

The worst-case RMS current requirement occurs when operating with $D = 0.5$. At this point, the above equation simplifies to $I_{RMS} = 0.5 \times I_{OUT}$. 

Figure 1. Adjustable Output Voltage
For the MAXM17505 system (IN) supply, ceramic capacitors are preferred due to their resilience to inrush surge currents typical of systems, and due to their low parasitic inductance that helps reduce the high-frequency ringing on the IN supply when the internal MOSFETs are turned off. Choose an input capacitor that exhibits less than +10°C temperature rise at the RMS input current for optimal circuit longevity.

Table 1. Selection Component Values

<table>
<thead>
<tr>
<th>V_IN (V)</th>
<th>V_OUT (V)</th>
<th>C_IN</th>
<th>C_OUT</th>
<th>R_U (kΩ)</th>
<th>R_B (kΩ)</th>
<th>f_{SW} (kHz)</th>
<th>R_T (kΩ)</th>
</tr>
</thead>
<tbody>
<tr>
<td>4.5 to 15</td>
<td>0.9</td>
<td>3 x 2.2µF 1206 100V</td>
<td>2 x 100µF 1210 4V</td>
<td>35.7</td>
<td>Open</td>
<td>300</td>
<td>68.1</td>
</tr>
<tr>
<td>4.5 to 15</td>
<td>1</td>
<td>3 x 2.2µF 1206 100V</td>
<td>2 x 100µF 1210 4V</td>
<td>39.2</td>
<td>357</td>
<td>300</td>
<td>68.1</td>
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<tr>
<td>4.5 to 15</td>
<td>1.2</td>
<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1 x 47µF 1210 4V</td>
<td>47.5</td>
<td>143</td>
<td>350</td>
<td>57.6</td>
</tr>
<tr>
<td>4.5 to 15</td>
<td>1.5</td>
<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1210 4V</td>
<td>59</td>
<td>88.7</td>
<td>350</td>
<td>57.6</td>
</tr>
<tr>
<td>4.5 to 15</td>
<td>1.8</td>
<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1210 4V</td>
<td>69.8</td>
<td>69.8</td>
<td>350</td>
<td>57.6</td>
</tr>
<tr>
<td>4.5 to 15</td>
<td>2.5</td>
<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1210 4V</td>
<td>97.6</td>
<td>54.9</td>
<td>350</td>
<td>57.6</td>
</tr>
<tr>
<td>4.5 to 15</td>
<td>3.3</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 47µF 1210 10V</td>
<td>127</td>
<td>47.5</td>
<td>400</td>
<td>49.9</td>
</tr>
<tr>
<td>7.5 to 15</td>
<td>5</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 47µF 1210 10V</td>
<td>196</td>
<td>43.2</td>
<td>500</td>
<td>Open</td>
</tr>
<tr>
<td>12 to 15</td>
<td>8</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 10µF 1210 16V</td>
<td>309</td>
<td>39.2</td>
<td>800</td>
<td>23.7</td>
</tr>
<tr>
<td>4.5 to 28</td>
<td>0.9</td>
<td>3 x 2.2µF 1206 100V</td>
<td>3 x 100µF 1210 4V</td>
<td>35.7</td>
<td>Open</td>
<td>200</td>
<td>100</td>
</tr>
<tr>
<td>4.5 to 28</td>
<td>1</td>
<td>3 x 2.2µF 1206 100V</td>
<td>2 x 100µF 1 x 47µF 1210 4V</td>
<td>39.2</td>
<td>357</td>
<td>238</td>
<td>86.6</td>
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<td>4.5 to 28</td>
<td>1.2</td>
<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1 x 47µF 1210 4V</td>
<td>47.5</td>
<td>143</td>
<td>285</td>
<td>71.5</td>
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<tr>
<td>4.5 to 28</td>
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<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1210 4V</td>
<td>59</td>
<td>88.7</td>
<td>350</td>
<td>57.6</td>
</tr>
<tr>
<td>4.5 to 28</td>
<td>1.8</td>
<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1210 4V</td>
<td>69.8</td>
<td>69.8</td>
<td>350</td>
<td>57.6</td>
</tr>
<tr>
<td>4.5 to 28</td>
<td>2.5</td>
<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1210 4V</td>
<td>97.6</td>
<td>54.9</td>
<td>350</td>
<td>57.6</td>
</tr>
<tr>
<td>4.5 to 28</td>
<td>3.3</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 47µF 1210 10V</td>
<td>127</td>
<td>47.5</td>
<td>400</td>
<td>49.9</td>
</tr>
<tr>
<td>7.5 to 28</td>
<td>5</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 47µF 1210 10V</td>
<td>196</td>
<td>43.2</td>
<td>500</td>
<td>Open</td>
</tr>
<tr>
<td>12 to 28</td>
<td>8</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 10µF 1210 16V</td>
<td>309</td>
<td>39.2</td>
<td>800</td>
<td>23.7</td>
</tr>
<tr>
<td>18.5 to 28</td>
<td>12</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 10µF 1210 16V</td>
<td>475</td>
<td>38.3</td>
<td>1200</td>
<td>15.8</td>
</tr>
<tr>
<td>4.5 to 40</td>
<td>1.2</td>
<td>3 x 2.2µF 1206 100V</td>
<td>2 x 100µF 1210 4V</td>
<td>47.5</td>
<td>143</td>
<td>200</td>
<td>100</td>
</tr>
<tr>
<td>4.5 to 40</td>
<td>1.5</td>
<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1 x 47µF 1210 4V</td>
<td>59</td>
<td>88.7</td>
<td>250</td>
<td>82.5</td>
</tr>
<tr>
<td>4.5 to 40</td>
<td>1.8</td>
<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1210 4V</td>
<td>69.8</td>
<td>69.8</td>
<td>300</td>
<td>68.1</td>
</tr>
<tr>
<td>4.5 to 40</td>
<td>2.5</td>
<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1210 4V</td>
<td>97.6</td>
<td>54.9</td>
<td>350</td>
<td>57.6</td>
</tr>
<tr>
<td>4.5 to 40</td>
<td>3.3</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 47µF 1210 10V</td>
<td>127</td>
<td>47.5</td>
<td>400</td>
<td>49.9</td>
</tr>
<tr>
<td>7.5 to 40</td>
<td>5</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 47µF 1210 10V</td>
<td>196</td>
<td>43.2</td>
<td>500</td>
<td>Open</td>
</tr>
<tr>
<td>12 to 40</td>
<td>8</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 10µF 1210 16V</td>
<td>309</td>
<td>39.2</td>
<td>800</td>
<td>23.7</td>
</tr>
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<td>18.5 to 40</td>
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<td>2 x 2.2µF 1206 100V</td>
<td>1 x 10µF 1210 16V</td>
<td>475</td>
<td>38.3</td>
<td>1200</td>
<td>15.8</td>
</tr>
<tr>
<td>4.5 to 60</td>
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<td>2 x 100µF 1210 4V</td>
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<tr>
<td>6 to 60</td>
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<td>3 x 2.2µF 1206 100V</td>
<td>1 x 100µF 1210 4V</td>
<td>97.6</td>
<td>54.9</td>
<td>277</td>
<td>73.2</td>
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<tr>
<td>8 to 60</td>
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<td>3 x 2.2µF 1206 100V</td>
<td>2 x 47µF 1210 10V</td>
<td>127</td>
<td>47.5</td>
<td>366</td>
<td>54.9</td>
</tr>
<tr>
<td>11 to 60</td>
<td>5</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 47µF 1210 10V</td>
<td>196</td>
<td>43.2</td>
<td>Open</td>
<td>500</td>
</tr>
<tr>
<td>17 to 60</td>
<td>8</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 10µF 1210 16V</td>
<td>309</td>
<td>39.2</td>
<td>888</td>
<td>21.5</td>
</tr>
<tr>
<td>25 to 60</td>
<td>12</td>
<td>2 x 2.2µF 1206 100V</td>
<td>1 x 10µF 1210 16V</td>
<td>475</td>
<td>38.3</td>
<td>1200</td>
<td>15.8</td>
</tr>
</tbody>
</table>
Output Capacitor Selection

The X7R ceramic output capacitors are preferred due to their stability over temperature in industrial applications. The minimum recommended output capacitor values in Table 1 are for desired output voltages to support a dynamic step load of 50% of the maximum output current in the application. For additional adjustable output voltages, the output capacitance value is derived from the following equation:

\[ C_{\text{OUT}} = \frac{I_{\text{STEP}} \times t_{\text{RESPONSE}}}{2 \times \Delta V_{\text{OUT}}} \]

\[ t_{\text{RESPONSE}} = \frac{0.33}{f_{C}} + \frac{1}{f_{SW}} \]

where \( I_{\text{STEP}} \) is the step load transient, \( t_{\text{RESPONSE}} \) is the response time of the controller, \( \Delta V_{\text{OUT}} \) is the allowable output ripple voltage during load transient, \( f_{C} \) is the target closed-loop crossover frequency, and \( f_{SW} \) is the switching frequency. Select \( f_{C} \) to be 1/9th of \( f_{SW} \) or 55kHz if the \( f_{SW} \) is greater than 500kHz.

Loop Compensation

The MAXM17505 integrates the internal compensation to stabilize the control loop. Only the device requires a combination of output capacitors and feedback resistors to program the closed-loop crossover frequency \( f_{C} \) at 1/9th of switching frequency. Use Table 1 to select component values to compensate with appropriate operating switching frequency. Connect a 0402 ceramic capacitor from CF to FB to correct frequency response with switching frequency below 500kHz. Place a 2.2pF capacitor for switching frequency range of 300kHz to 500kHz.

Setting the Switching Frequency (RT)

The switching frequency range of 100kHz to 1.8MHz are recommended from Table 1 for desired input and output voltages. The switching frequency of MAXM17505 can be programmed by using a single resistor \( R_{RT} \) connected from the RT pin to SGND. The calculation of \( R_{RT} \) resistor is given by the following equation:

\[ R_{RT} \approx \frac{21000}{f_{SW}} - 1.7 \]

where \( R_{RT} \) is in kΩ and \( f_{SW} \) is in kHz. Leaving the RT pin open to operate at the default switching frequency of 500kHz.

Soft-Start Capacitor Selection

The device implements an adjustable soft-start operation to reduce inrush current during startup. A capacitor \( (C_{SS}) \) connected from the SS pin to SGND to program the soft-start time.

The selected output capacitance \( (C_{SEL}) \) and the output voltage \( (V_{OUT}) \) determine the minimum value of \( C_{SS} \), as shown by the following equation:

\[ C_{SS} \geq 28 \times 10^{-3} \times C_{SEL} \times V_{OUT} \]

where \( C_{SS} \) is in nF and \( C_{SEL} \) is in µF.

The value of the soft-start capacitor is calculated from the desired soft-start time as follows:

\[ t_{SS} \approx \frac{C_{SS}}{5.55} \]

where \( t_{SS} \) is in ms and \( C_{SS} \) is in nF.

Detailed Description

The MAXM17505 is a complete step-down DC-DC power supply that delivers up to 1.7A output current. The device provides a programmable output voltage to regulate up to 12V through external resistor dividers from an input voltage range of 4.5V to 60V. The recommended input voltage in Table 1 is selected highly enough to support the desired output voltage and load current. The device includes an adjustable frequency feature range from 100kHz to 1.8MHz to reduce sizes of input and output capacitors. The Functional Diagram shows a complete internal block diagram of the MAXM17505 power module.

Input Undervoltage-Lockout Level

The MAXM17505 contains an internal pullup resistor (3.3MΩ) from EN to IN to have a default startup voltage. The device offers an adjustable input undervoltage-lockout level to set the voltage at which the device is turned on by a single resistor connecting from EN/UVLO to SGND as equation:

\[ R_{ENU} \approx \frac{3.3 \times 1215}{(V_{INU} - 1.215)} \]

where \( R_{ENU} \) is in kΩ and \( V_{INU} \) is the voltage at which the device is required to turn on the device. Ensure that \( V_{INU} \) is high enough to support the \( V_{OUT} \). See Table 1 to set the proper \( V_{INU} \) voltage greater than or equal the minimum input voltage for each desired output voltage.
Mode Selection (MODE)
The MAXM17505 features a MODE pin to configure the device operating in PWM, PFM, or DCM control schemes. The device operates in PFM mode at light loads if the MODE pin is open. If the MODE pin connects to ground, the device operates in constant-frequency PWM mode at all loads. The device operates in constant-frequency DCM mode at light loads when the MODE pin connects to V\textsubscript{CC}. State changes of the MODE operation are only at power-up and ignore during normal operation.

PWM Mode Operation
In PWM mode, the step-down controller is switching a constant-frequency at all loads with a minimum sink current limit threshold (-1.8A, typ) at light load. The PWM mode of operation gives lower efficiency at light loads compared to PFM and DCM modes of operation. However, the PWM mode of operation is useful in applications sensitive to switching frequency.

PFM Mode Operation
In PFM mode, the controller forces the peak inductor current in order to feed the light loads and maintain high efficiency. If the load is lighter than the average PFM value, the output voltage will exceed 102.3% of the feedback threshold and the controller enters into a hibernation mode, turning off most of the internal blocks. The device exits hibernation mode, and starts switching again, once the output voltage is discharged to 101.1% of the feedback threshold. In this mode, the behavior resembles PWM operation (with occasional pulse-skipping), where the inductor current does not need to reach the light-load level.

PFM mode offers the advantage of increased efficiency at light loads due to a lower quiescent current drawn from the supply. However, the output-voltage ripple is also increased as compared to the PWM or DCM modes of operation, and the switching frequency is not constant at light loads.

DCM Mode Operation
DCM mode features constant frequency operation down to lighter loads than PFM mode, accomplished by not skipping pulses. DCM efficiency performance lies between the PWM and PFM modes.

External Frequency Synchronization (SYNC)
The device can be synchronized by an external clock signal on the SYNC pin. The external synchronization clock frequency must be between 1.1 x f\textsubscript{SW} and 1.4 x f\textsubscript{SW}, where f\textsubscript{SW} is the frequency programmed by the RT resistor. The minimum external clock high pulse width and amplitude should be greater than 50ns and 2.1V respectively. The minimum external clock low pulse width should be greater than 160ns, and the maximum external clock low pulse amplitude should be less than 0.8V. Table 1 provides recommended synchronous frequency ranges for desired output voltages. Connect the SYNC pin to SGND if it is not used.

RESET Output
The device includes a RESET comparator to monitor the output for undervoltage and overvoltage conditions. The open-drain RESET output requires an external pullup resistor from 10k\textohm to 100k\textohm to V\textsubscript{CC} pin or maximum 6V voltage source. RESET goes high impedance after the regulator output increases above 95% of the designed nominal regulated voltage. RESET goes low when the regulator output voltage drops below 92% of the nominal regulated voltage. RESET also goes low during thermal shutdown.

Overcurrent Protection (OCP)
The MAXM17505 is provided with a robust overcurrent protection (OCP) scheme that protects the module under overload and output short-circuit conditions. A cycle-by-cycle peak current limit turns off the high-side MOSFET whenever the high-side switch current exceeds an internal limit of 2.8A (typ). The module enters hiccup mode of operation either after one occurrence of the runaway current limit 3.4A (typ) or when the FB node goes below 0.58V of its nominal regulation threshold after soft-start is complete. In hiccup mode, the module is protected by suspending switching for a hiccup timeout period of 32,768 clock cycles. Once the hiccup timeout period expires, soft-start is attempted again. Hiccup mode of operation ensures low power dissipation under output overload or short-circuit conditions. Note that when soft-start is attempted under overload condition, if feedback voltage does not exceed 0.58V, the device switches at half the programmed switching frequency.
The MAXM17505 is designed to support a maximum load current of 3.5A. The inductor ripple current is calculated as follows:

\[
\Delta I = \left( \frac{V_{IN} - V_{OUT} - 0.426 \times I_{OUT}}{L \times f_{SW}} \right) \times \left( \frac{V_{OUT} + 0.251 \times I_{OUT}}{V_{IN} - 0.175 \times I_{OUT}} \right)
\]

where,

- \( V_{OUT} \) = Steady-state output voltage
- \( V_{IN} \) = Operating input voltage
- \( f_{SW} \) = Switching frequency in Hz
- \( L \) = Power module output inductance (10µH ±20%)
- \( I_{OUT} \) = Required output (load) current

The following condition should be satisfied at the desired load current (\( I_{OUT} \)).

\[
I_{OUT} + \frac{\Delta I}{2} < 2.4
\]

**Thermal Fault Protection**

The MAXM17505 features a thermal-fault protection circuit. When the junction temperature rises above +165°C (typ), a thermal sensor activates the fault latch, pulls down the RESET output, and shuts down the regulator. The thermal sensor restarts the controllers after the junction temperature cools by 10°C (typ). The soft-start resets during thermal shutdown.

**Power Dissipation and Output-Current Derating**

The MAXM17505 output current needs to be derated if the device needs to be operated in a high ambient-temperature environment. The amount of current-derating depends upon the input voltage, output voltage, and ambient temperature. The derating curves in TOC43 from the *Typical Operating Characteristics* section can be used as guidelines. The curves are based on simulating thermal resistance model (\( \psi_{JT} \)), measuring thermal resistance (\( \psi_{TA} \)), and measuring power dissipation (\( P_{DMAX} \)) on the bench.

The maximum allowable power losses can be calculated using the following equation:

\[
P_{DMAX} = \frac{T_{JMAX} - T_{A}}{\theta_{JA}}
\]

where:

- \( P_{DMAX} \) is the maximum allowed power losses with maximum allowed junction temperature.
- \( T_{JMAX} \) is the maximum allowed junction temperature.
- \( T_{A} \) is operating ambient temperature.
- \( \theta_{JA} \) is the junction to ambient thermal resistance.

**PCB Layout Guidelines**

Careful PCB layout is critical to achieving low switching losses and clean, stable operation.

Use the following guidelines for good PCB layout:

- Keep the input capacitors as close as possible to the IN and PGND pins.
- Keep the output capacitors as close as possible to the OUT and PGND pins.
- Keep the resistive feedback dividers as close as possible to the FB pin.
- Connect all of the PGND connections to as large as copper plane area as possible on the top layer.
- Use multiple vias to connect internal PGND planes to the top layer PGND plane.
- Do not keep any solder mask on EP1, EP2, and EP3 on bottom layer. Keeping solder mask on exposed pads decreases the heat dissipating capability.
- Keep the power traces and load connections short. This practice is essential for high efficiency. Using thick copper PCBs (2oz vs. 1oz) can enhance full-load efficiency. Correctly routing PCB traces is a difficult task that must be approached in terms of fractions of centimeters, where a single milliohm of excess trace resistance causes a measurable efficiency penalty.
MAXM17505 4.5V to 60V, 1.7A High-Efficiency, DC-DC Step-Down Power Module with Integrated Inductor

Layout Recommendation

Chip Information
PROCESS: BICMOS

Ordering Information

<table>
<thead>
<tr>
<th>PART</th>
<th>TEMP RANGE</th>
<th>MSL</th>
<th>PACKAGE</th>
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<tbody>
<tr>
<td>MAXM17505ALJ+T</td>
<td>-40°C to +125°C</td>
<td>3</td>
<td>29 SiP</td>
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+Denotes a lead(Pb)-free/RoHS-compliant package.
T = Tape and reel.
Revision History

<table>
<thead>
<tr>
<th>REVISION NUMBER</th>
<th>REVISION DATE</th>
<th>DESCRIPTION</th>
<th>PAGES CHANGED</th>
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<tbody>
<tr>
<td>0</td>
<td>11/14</td>
<td>Initial release</td>
<td>—</td>
</tr>
<tr>
<td>1</td>
<td>4/15</td>
<td>Added application recommendation to avoid potential latch-up issue on EN pin and added MSL 3 rating</td>
<td>11, 18</td>
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<tr>
<td>2</td>
<td>11/16</td>
<td>Updated Package Thermal Characteristics and notes sections, updated Pin 4 in the Pin Description section, and updated the Loop Compensation section</td>
<td>2, 11, 15</td>
</tr>
<tr>
<td>3</td>
<td>5/20</td>
<td>Updated the General Description, Benefits and Features and Loop Compensation sections, and Table 1; added the Overcurrent Protection (OCP) section, and TOC44 and TOC45; replaced the Input-Voltage Range section</td>
<td>1, 10, 14–18</td>
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