General Description

The MAX745 provides all functions necessary for charging lithium-ion (Li+) battery packs. It provides a regulated charging current of up to 4A without getting hot, and a regulated voltage with only ±0.75% total error at the battery terminals. It uses low-cost, 1% resistors to set the output voltage, and a low-cost N-channel MOSFET as the power switch.

The MAX745 regulates the voltage set point and charging current using two loops that work together to transition smoothly between voltage and current regulation. The per-cell battery voltage regulation limit is set between 4V and 4.4V using standard 1% resistors, and then the number of cells is set from 1 to 4 by pin-strappping. Total output voltage error is less than ±0.75%.

For a similar device with an SMBus™ microcontroller interface and the ability to charge NiCd and NiMH cells, refer to the MAX1647 and MAX1648. For a low-cost Li+ charger using a linear-regulator control scheme, refer to the MAX846A.

Features

♦ Charges 1 to 4 Li+ Battery Cells
♦ ±0.75% Voltage-Regulation Accuracy Using 1% Resistors
♦ Provides up to 4A without Excessive Heating
♦ 90% Efficient
♦ Uses Low-Cost Set Resistors and N-Channel Switch
♦ Up to 24V Input
♦ Up to 18V Maximum Battery Voltage
♦ 300kHz Pulse-Width Modulated (PWM) Operation Low-Noise, Small Components
♦ Stand-Alone Operation—No Microcontroller Needed

Applications

Li+ Battery Packs
Desktop Cradle Chargers
Cellular Phones
Notebook Computers
Hand-Held Instruments

Ordering Information

<table>
<thead>
<tr>
<th>PART</th>
<th>TEMP RANGE</th>
<th>PIN-PACKAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAX745C/D</td>
<td>0°C to +70°C</td>
<td>Dice*</td>
</tr>
<tr>
<td>MAX745EAP</td>
<td>-40°C to +85°C</td>
<td>20 SSOP</td>
</tr>
</tbody>
</table>

*Dice are tested at TA = +25°C.

Pin Configuration appears at end of data sheet.

Typical Operating Circuit

SMBus is a trademark of Intel Corp.
Switch-Mode Lithium-Ion Battery Charger

ABSOLUTE MAXIMUM RATINGS

DCIN to GND ............................................................-0.3V to 26V
BST, DHI to GND ......................................................-0.3V to 30V
BST to LX ....................................................................-0.3V to 6V
DHI to LX ......................................................(LX - 0.3V) to (BST + 0.3V)
LX to GND ................................................-0.3V to (DCIN + 0.3V)
VL to GND ................................................-0.3V to 6V
CELL0, CELL1, IBAT, STATUS, CCI, CV, REF, SETI, VADJ, DLO, THM/SHDN to GND.........-0.3V to (VL + 0.3V)
BATT, CS to GND ............................................................-0.3V to 20V
PGND to GND ............................................................-0.3V to 0.3V
VL Current .............................................................50mA
Continuous Power Dissipation (TA = +70°C) ................640mW
SSOP (derate 8.00mW/°C above +70°C)..............400mW
Operating Temperature Range ......................-40°C to +85°C
Storage Temperature.....................................-60°C to +150°C
Lead Temperature (soldering, 10s) ..............+200°C

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(VDCIN = 18V, VBATT = 8.4V, TA = 0°C to +85°C. Typical values are at TA = +25°C, unless otherwise noted.)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>DCIN Input Voltage Range</td>
<td>6.0V &lt; VDCIN &lt; 24V, logic inputs = VL</td>
<td>4</td>
<td>6</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>DCIN Quiescent Supply Current</td>
<td>6.0V &lt; VDCIN &lt; 24V, no load</td>
<td>5.15</td>
<td>5.40</td>
<td>5.65</td>
<td>V</td>
</tr>
<tr>
<td>VL Output Voltage</td>
<td>TA = +25°C</td>
<td>4.17</td>
<td>4.2</td>
<td>4.24</td>
<td>V</td>
</tr>
<tr>
<td>REF Output Voltage</td>
<td>6.0V &lt; VDCIN &lt; 24V</td>
<td>4.16</td>
<td>4.2</td>
<td>4.24</td>
<td></td>
</tr>
<tr>
<td>REF Output Load Regulation</td>
<td>0 &lt; IREF &lt; 1mA</td>
<td>10</td>
<td>20</td>
<td>mV/mA</td>
<td></td>
</tr>
<tr>
<td>SWITCHING REGULATOR</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Oscillator Frequency</td>
<td></td>
<td>270</td>
<td>300</td>
<td>330</td>
<td>kHz</td>
</tr>
<tr>
<td>DHI Maximum Duty Cycle</td>
<td></td>
<td>89</td>
<td>93</td>
<td>%</td>
<td></td>
</tr>
<tr>
<td>DHI On-Resistance</td>
<td>Output high or low</td>
<td>4</td>
<td>7</td>
<td>Ω</td>
<td></td>
</tr>
<tr>
<td>DLO On-Resistance</td>
<td>Output high or low</td>
<td>6</td>
<td>14</td>
<td>Ω</td>
<td></td>
</tr>
<tr>
<td>BATT Input Current</td>
<td>VL &lt; 3.2V, VBATT = 12V</td>
<td>5</td>
<td></td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td></td>
<td>VL &gt; 5.15V, VBATT = 12V</td>
<td>500</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CS Input Current</td>
<td>VL &lt; 3.2V, VCS = 12V</td>
<td>5</td>
<td></td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td></td>
<td>VL &gt; 5.15V, VCS = 12V</td>
<td>400</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>BATT, CS Input Voltage Range</td>
<td>4V &lt; VBATT &lt; 16V</td>
<td>0</td>
<td>19</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>CS to BATT Offset Voltage</td>
<td>(Note 1)</td>
<td>±1.5</td>
<td></td>
<td>mV</td>
<td></td>
</tr>
<tr>
<td>CS to BATT Current-Sense Voltage</td>
<td>SETI = VREF (full scale)</td>
<td>170</td>
<td>185</td>
<td>205</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>SETI = 400mV</td>
<td>14</td>
<td>18</td>
<td>22</td>
<td>mV</td>
</tr>
<tr>
<td>Absolute Voltage Accuracy</td>
<td>Not including VADJ resistor tolerance</td>
<td>-0.65</td>
<td>+0.65</td>
<td>%</td>
<td></td>
</tr>
<tr>
<td></td>
<td>With 1% tolerance VADJ resistors</td>
<td>-0.75</td>
<td>+0.75</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Switch-Mode Lithium-Ion Battery Charger

ELECTRICAL CHARACTERISTICS (continued)
(V\textsubscript{DCIN} = 18V, VB\textsubscript{ATT} = 8.4V. TA = 0°C to +85°C. Typical values are at TA = +25°C, unless otherwise noted.)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>ERROR AMPLIFIERS</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>GMV Amplifier Transconductance</td>
<td></td>
<td>800</td>
<td>200</td>
<td>320</td>
<td>μA/V</td>
</tr>
<tr>
<td>GMI Amplifier Transconductance</td>
<td></td>
<td>200</td>
<td>130</td>
<td>130</td>
<td>μA/V</td>
</tr>
<tr>
<td>GMV Amplifier Output Current</td>
<td></td>
<td>±130</td>
<td>200</td>
<td>320</td>
<td>μA/V</td>
</tr>
<tr>
<td>GMI Amplifier Output Current</td>
<td></td>
<td>±320</td>
<td>130</td>
<td>130</td>
<td>μA/V</td>
</tr>
<tr>
<td>CCI Clamp Voltage with Respect to CCV</td>
<td>1.1V &lt; V\textsubscript{CCV} &lt; 3.5V</td>
<td>25</td>
<td>80</td>
<td>200</td>
<td>mV</td>
</tr>
<tr>
<td>CCV Clamp Voltage with Respect to CCI</td>
<td>1.1V &lt; V\textsubscript{CCI} &lt; 3.5V</td>
<td>25</td>
<td>80</td>
<td>200</td>
<td>mV</td>
</tr>
</tbody>
</table>

**CONTROL INPUTS/OUTPUTS**

<table>
<thead>
<tr>
<th>CONDITION</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>CELL0, CELL1 Input Bias Current</td>
<td>-1</td>
<td>μA</td>
</tr>
<tr>
<td>SETI Input Voltage Range</td>
<td>0</td>
<td>VREF</td>
</tr>
<tr>
<td>VADJ Adjustment Range</td>
<td>10</td>
<td>%</td>
</tr>
<tr>
<td>SETI, VADJ Input Bias Current</td>
<td>-10</td>
<td>+10</td>
</tr>
<tr>
<td>VADJ Input Voltage Range</td>
<td>0</td>
<td>VREF</td>
</tr>
<tr>
<td>THM/SHDN Rising Threshold</td>
<td>2.20</td>
<td>2.3</td>
</tr>
<tr>
<td>THM/SHDN Falling Threshold</td>
<td>2.01</td>
<td>2.1</td>
</tr>
<tr>
<td>STATUS Output Low Voltage</td>
<td>0.2</td>
<td>V</td>
</tr>
<tr>
<td>STATUS Output Leakage Current</td>
<td>1</td>
<td>μA</td>
</tr>
<tr>
<td>IBAT Output Current vs. Current-Sense Voltage</td>
<td>0.9</td>
<td>μA/mV</td>
</tr>
<tr>
<td>IBAT Compliance Voltage Range</td>
<td>0</td>
<td>2</td>
</tr>
</tbody>
</table>

ELECTRICAL CHARACTERISTICS
(V\textsubscript{DCIN} = 18V, VB\textsubscript{ATT} = 8.4V. TA = -40°C to +85°C, unless otherwise noted. Limits over temperature are guaranteed by design.)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>SUPPLY AND REFERENCE</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VL Output Voltage</td>
<td>6.0V &lt; V\textsubscript{DCIN} &lt; 24V, no load</td>
<td>5.10</td>
<td>5.70</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>REF Output Voltage</td>
<td>6.0V &lt; V\textsubscript{DCIN} &lt; 24V</td>
<td>4.14</td>
<td>4.26</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td><strong>SWITCHING REGULATOR</strong> (Note 1)**</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Oscillator Frequency</td>
<td></td>
<td>260</td>
<td>340</td>
<td></td>
<td>kHz</td>
</tr>
<tr>
<td>DHI On-Resistance</td>
<td>Output high or low</td>
<td>7</td>
<td></td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>DLO On-Resistance</td>
<td>Output high or low</td>
<td>14</td>
<td></td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>CS to BATT Full-Scale Current-Sense Voltage</td>
<td></td>
<td>165</td>
<td>205</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>Absolute Voltage Accuracy</td>
<td>Not including VADJ resistors</td>
<td>-1.0</td>
<td>1.0</td>
<td></td>
<td>%</td>
</tr>
</tbody>
</table>

**Note 1:** When V\textsubscript{SETI} = 0V, the battery charger turns off.
**Switch-Mode Lithium-Ion Battery Charger**

**Typical Operating Characteristics**

\( T_A = +25^\circ C, \quad V_{DCIN} = 18V, \quad V_{BATT} = 4.2V, \quad \text{CELL0} = \text{CELL1} = \text{GND}, \quad C_{VL} = 4.7\mu F \quad C_{REF} = 0.1\mu F. \) Circuit of Figure 1, unless otherwise noted.

---

**Battery Voltage vs. Charging Current**

- R1 = 0.2Ω
- R16 = SHORT
- R12 = OPEN CIRCUIT

**Current-Sense Voltage vs. SetI Voltage**

- \( R_1 = 0.2\Omega \)

**Reference Voltage vs. Temperature**

- \( 4.195 \) to \( 4.205 \) V

**Voltage Limit vs. VADJ Voltage**

- \( 3.90 \) to \( 4.40 \) V

**VL Load Regulation**

- \( 5.30 \) to \( 5.50 \) V

**Reference Load Regulation**

- \( 4.15 \) to \( 4.25 \) V
### Detailed Description

The MAX745 is a switch-mode, Li+ battery charger that can achieve 90% efficiency. The charge voltage and current are set independently by external resistor-dividers at SETI and VADJ, and at pin connections at CELL0 and CELL1. VADJ is tied to a 1% tolerance external resistor-divider to adjust the voltage set point by 10%, eliminating the need for precision 0.1% resistors. The input voltage range is 0V to VREF.

The MAX745 consists of a current-mode, pulse-width-modulated (PWM) controller and two transconductance error amplifiers: one for regulating current (GMI) and the other for regulating voltage (GMV) (Figure 2). The error amplifiers are controlled through the SETI and VADJ pins. Whether the MAX745 is controlling voltage or current at any time depends on the battery state. If the battery is discharged, the MAX745 output reaches the current-regulation limit before the voltage limit, causing the system to regulate current. As the battery charges, the voltage rises to the point where the voltage limit is reached and the charger switches to regulating voltage. The STATUS pin indicates whether the charger is regulating current or voltage.

### Voltage Control

To set the voltage limit on the battery, connect a resistor-divider to VADJ from REF. A 0V to VREF change at VADJ sets a ±5% change in the battery limit voltage around 4.2V. Since the 0 to 4.2V range on VADJ results in only a 10% change on the voltage limit, the resistor-divider’s accuracy does not need to be as high as the output voltage accuracy. Using 1% resistors for the voltage dividers typically results in no more than 0.1% degradation in output voltage accuracy. VADJ is internally buffered so that high-value resistors can be used to set the output voltage. When the voltage at VADJ is

---

<table>
<thead>
<tr>
<th>PIN</th>
<th>NAME</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>IBAT</td>
<td>Current-Sense Amplifier’s Analog Current-Source Output. See the Monitoring Charge Current section for a detailed description.</td>
</tr>
<tr>
<td>2</td>
<td>DCIN</td>
<td>Charger Input Voltage. Bypass DCIN with a 0.1µF capacitor.</td>
</tr>
<tr>
<td>3</td>
<td>VL</td>
<td>Chip Power Supply. Output of the 5.4V linear regulator from DCIN. Bypass VL with a 4.7µF capacitor.</td>
</tr>
<tr>
<td>4</td>
<td>CCV</td>
<td>Voltage-Regulation-Loop Compensation Point</td>
</tr>
<tr>
<td>5</td>
<td>CCI</td>
<td>Current-Regulation-Loop Compensation Point</td>
</tr>
<tr>
<td>6</td>
<td>THM/SHDN</td>
<td>Thermistor Sense-Voltage Input. THM/SHDN also performs the shutdown function. If pulled low, the charger turns off.</td>
</tr>
<tr>
<td>7</td>
<td>REF</td>
<td>4.2V Reference Voltage Output. Bypass REF with a 0.1µF or greater capacitor.</td>
</tr>
<tr>
<td>8</td>
<td>VADJ</td>
<td>Voltage-Adjustment Pin. VADJ is tied to a 1% tolerance external resistor-divider to adjust the voltage set point by 10%, eliminating the need for precision 0.1% resistors. The input voltage range is 0V to VREF.</td>
</tr>
<tr>
<td>9</td>
<td>SETI</td>
<td>SETI is externally tied to the resistor-divider between REF and GND to set the charging current.</td>
</tr>
<tr>
<td>10</td>
<td>GND</td>
<td>Analog Ground</td>
</tr>
<tr>
<td>11, 12</td>
<td>CELL1, CELL0</td>
<td>Logic Inputs to Select Cell Count. See Table 1 for cell-count programming.</td>
</tr>
<tr>
<td>13</td>
<td>STATUS</td>
<td>An open-drain MOSFET sinks current when in current-regulation mode, and is high impedance when in voltage-regulation mode. Connect STATUS to VL through a 1kΩ to 100kΩ pullup resistor. STATUS can also drive an LED for visual indication of regulation mode (see MAX745 EV kit). Leave STATUS floating if not used.</td>
</tr>
<tr>
<td>14</td>
<td>BATT</td>
<td>Battery-Voltage-Sense Input and Current-Sense Negative Input</td>
</tr>
<tr>
<td>15</td>
<td>CS</td>
<td>Current-Sense Positive Input</td>
</tr>
<tr>
<td>16</td>
<td>PGND</td>
<td>Power Ground</td>
</tr>
<tr>
<td>17</td>
<td>DLO</td>
<td>Low-Side Power MOSFET Driver Output</td>
</tr>
<tr>
<td>18</td>
<td>DHI</td>
<td>High-Side Power MOSFET Driver Output</td>
</tr>
<tr>
<td>19</td>
<td>LX</td>
<td>Power Connection for the High-Side Power MOSFET Source</td>
</tr>
<tr>
<td>20</td>
<td>BST</td>
<td>Power Input for the High-Side Power MOSFET Driver</td>
</tr>
</tbody>
</table>
**Switch-Mode Lithium-Ion Battery Charger**

VREF / 2, the voltage limit is 4.2V. Table 1 defines the battery cell count.

The battery limit voltage is set by the following:

\[ V_{\text{BATT}} = (\text{cell count}) \times \left( \frac{V_{\text{ADJ}} - \frac{1}{2} V_{\text{REF}}}{9.523} \right) \]

Solving for VADJ, we get:

\[ V_{\text{ADJ}} = \frac{9.523 V_{\text{BATT}}}{(\text{cell count})} - 9.023 V_{\text{REF}} \]

Set VADJ by choosing a value for R11 (typically 100kΩ), and determine R3 by:

\[ R3 = [1 - (V_{\text{ADJ}} / V_{\text{REF}})] \times R11 \] (Figure 1)

**Table 1. Cell-Count Programming Table**

<table>
<thead>
<tr>
<th>CELL0</th>
<th>CELL1</th>
<th>CELL COUNT</th>
</tr>
</thead>
<tbody>
<tr>
<td>GND</td>
<td>GND</td>
<td>1</td>
</tr>
<tr>
<td>VL</td>
<td>GND</td>
<td>2</td>
</tr>
<tr>
<td>GND</td>
<td>VL</td>
<td>3</td>
</tr>
<tr>
<td>VL</td>
<td>VL</td>
<td>4</td>
</tr>
</tbody>
</table>

where VREF = 4.2V and cell count is 1, 2, 3, 4 (Table 1).

The voltage-regulation loop is compensated at the CCV pin. Typically, a series-resistor-capacitor combination can be used to form a pole-zero doublet. The pole introduced rolls off the gain starting at low frequencies. The zero of the doublet provides sufficient AC gain at mid-frequencies. The output capacitor (C1) rolls off the mid-frequency gain to below unity. This guarantees stability before encountering the zero introduced by the C1’s equivalent series resistance (ESR). The GMV amplifier’s output is internally clamped to between one-fourth and three-fourths of the voltage at REF.

**Current Control**

The charging current is set by a combination of the current-sense resistor value and the SET1 pin voltage. The current-sense amplifier measures the voltage across the current-sense resistor, between CS and BATT. The current-sense amplifier’s gain is 6. The voltage on SET1 is buffered and then divided by 4. This voltage is compared to the current-sense amplifier’s output. Therefore, full-scale current is accomplished by connecting SET1 to REF. The full-scale charging current (IFS) is set by the following:

\[ I_{\text{FS}} = 185mV / R1 \] (Figure 1)
Switch-Mode Lithium-Ion Battery Charger

To set currents below full scale without changing R1, adjust the voltage at SETI according to the following formula:

\[
I_{CHG} = I_{FS} \left( \frac{V_{SETI}}{V_{REF}} \right)
\]

A capacitor at CCI sets the current-feedback loop’s dominant pole. While the current is in regulation, CCV voltage is clamped to within 80mV of the CCI voltage. This prevents the battery voltage from overshooting when the voltage setting is changed. The converse is true when the voltage is in regulation and the current setting is changed. Since the linear range of CCI or CCV is about 2V (1.5V to 3.5V), the 80mV clamp results in negligible overshoot when the loop switches from voltage regulation to current regulation, or vice versa.

**Monitoring Charge Current**

The battery-charging current can be externally monitored by placing a scaling resistor (RIBAT) between IBAT and GND. IBAT is the output of a voltage-controlled current source, with output current given by:

\[
I_{IBAT} = \frac{0.9 \mu A}{mV} \times V_{SENSE}
\]

where VSENSE is the voltage across the current-sense resistor (in millivolts) given by:

\[
V_{SENSE} = V_{CS} - V_{BATT} = I_{CHG} \times R1
\]

The voltage across RIBAT is then given by:

\[
V_{IBAT} = 0.9 \times 10^{-3} \times I_{CHG} \times R1 \times R_{IBAT}
\]

RIBAT must be chosen to limit VIBAT to voltages below 2V for the maximum charging current. Connect IBAT to GND if unused.

**PWM Controller**

The battery voltage or current is controlled by a current-mode, PWM DC/DC converter controller. This controller drives two external N-channel MOSFETs, which control power from the input source. The controller sets the switched voltages pulse width so that it supplies the desired voltage or current to the battery. Total component cost is reduced by using a dual, N-channel MOSFET.

The heart of the PWM controller is a multi-input comparator. This comparator sums three input signals to determine the switched signal’s pulse width, setting the battery voltage or current. The three signals are the current-sense amplifier’s output, the GMV or GMI error amplifier’s output, and a slope-compensation signal that ensures that the current-control loop is stable.

The PWM comparator compares the current-sense amplifier’s output to the lower output voltage of either the GMV or GMI amplifiers (the error voltage). This current-mode feedback reduces the effect of the inductor on the output filter LC formed by the output inductor (L1) and C1 (Figure 1). This makes stabilizing the circuit much easier, since the output filter changes to a first-order RC from a complex, second-order RLC.

![Functional Diagram](image-url)

Figure 2. Functional Diagram
Switch-Mode Lithium-Ion Battery Charger

### MOSFET Drivers

The MAX745 drives external N-channel MOSFETs to switch the input source generating the battery voltage or current. Since the high-side N-channel MOSFET's gate must be driven to a voltage higher than the input source voltage, a charge pump is used to generate such a voltage. The capacitor (C7) charges through D2 to approximately 5V when the synchronous rectifier (M1B) turns on (Figure 1). Since one side of C7 is connected to LX (the source of M1A), the high-side driver (DHI) drives the gate up to the voltage at BST, which is greater than the input voltage while the high-side MOSFET is on.

The synchronous rectifier (M1B) behaves like a diode but has a smaller voltage drop, improving efficiency. A small dead time is added between the time when the high-side MOSFET is turned off and when the synchronous rectifier is turned on, and vice versa. This prevents crowbar currents during switching transitions. Place a Schottky rectifier from LX to ground (D1, across M1B’s drain and source) to prevent the synchronous rectifier’s body diode from conducting during the dead time. The body diode typically has slower switching-recovery times, so allowing it to conduct degrades efficiency. D1 can be omitted if efficiency is not a concern, but the resulting increased power dissipation in the synchronous rectifier must be considered.

Since the BST capacitor is charged while the synchronous rectifier is on, the synchronous rectifier may not be replaced by a rectifier. The BST capacitor will not fully charge without the synchronous rectifier, leaving the high-side MOSFET with insufficient gate drive to turn on. However, the synchronous rectifier can be replaced with a small MOSFET (such as a 2N7002) to guarantee that the BST capacitor is allowed to charge. In this case, the majority of the high charging currents are carried by D1, and not by the synchronous rectifier.

### Minimum Input Voltage

The input voltage to the charger circuit must be greater than the maximum battery voltage by approximately 2V so the charger can regulate the voltage properly. The input voltage can have a large AC-ripple component when operating from a wall cube. The voltage at the low point of the ripple waveform must still be approximately 2V greater than the maximum battery voltage.

Using components as indicated in Figure 1, the minimum input voltage can be determined by the following formula:

\[
\text{VIN} \times \frac{[\text{VBATT} + \text{VD6} + \text{ICHG} (\text{RDS(ON)} + \text{RL} + \text{R1})]}{0.89}
\]

where:
- \( \text{VIN} \) is the input voltage;
- \( \text{VD6} \) is the voltage drop across D6 (typically 0.4V to 0.5V);
- \( \text{ICHG} \) is the charging current;
- \( \text{RDS(ON)} \) is the high-side MOSFET M1A's on-resistance;
- \( \text{RL} \) is the the inductor’s series resistance;
- \( \text{R1} \) is the current-sense resistor R1’s value.

### Pin Configuration

![Pin Configuration Diagram]

**Chip Information**

TRANSISTOR COUNT: 1695

SUBSTRATE CONNECTED TO GND