-5V/Adjustable, Negative-Output, Inverting, Current-Mode PWM Regulators

**General Description**

The MAX735 and MAX755 are CMOS, inverting switch-mode regulators with internal power MOSFETs. The MAX755 operates from a ±2.7V to ±9V input and generates an adjustable negative output. 1W output power is guaranteed when powered from a ±4.5V input. The MAX735 operates from a ±4.0V to ±6.2V output. 200mA output current is guaranteed for inputs greater than ±4.5V. Quiescent supply current for the MAX735 is typically 1.8mA, and a shutdown mode reduces this to 10μA. These power-conserving features, along with high efficiency and applications circuits that lend themselves to miniaturization, make the MAX735/MAX755 excel in a broad range of on-card and portable-equipment applications.

The MAX735/MAX755 employ a high-performance current-mode pulse-width modulation (PWM) control scheme to provide tight output-voltage regulation and low subharmonic noise. The fixed-frequency oscillator is factory-trimmed to 160kHz, allowing for easy noise filtering. The regulators are production tested in actual applications circuits, and output accuracy is guaranteed to within ±5% over all specified conditions of line, load, and temperature.

The input-to-output differential of the MAX755 is limited to VIN + |VOUT| ≤ 11.7V.

For an adjustable output device with a wider input voltage range, refer to the MAX759 data sheet. For a fixed-5V part with a wider input voltage range, refer to the MAX739 data sheet. For fixed -12V and -15V versions, see the MAX736 and MAX737 data sheets. For lower-power applications, refer to the MAX639V/636/637 data sheet.

**Applications**

- Board-Level DC-DC Conversion
- Battery-Powered Equipment
- Computer Peripherals

**Typical Operating Circuit**

![Typical Operating Circuit Diagram]

**Features**

- Converts ±2.7V to ±9V Input to Adjustable Negative Output (MAX755)
- Converts ±4.0V to ±6.2V Input to -5V Output (MAX735)
- 1W Guaranteed Output Power (VOUT ≥ 4.5V)
- 78% Typical Efficiency
- 1.6mA Quiescent Current (MAX735)
- 10μA Shutdown Mode
- 160kHz Fixed-Frequency Oscillator
- Current-Mode PWM - Low Noise and Jitter
- Soft-Start
- Simple Application Circuit

**Ordering Information**

<table>
<thead>
<tr>
<th>PART</th>
<th>TEMP. RANGE</th>
<th>PIN-PACKAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAX735CPA</td>
<td>0°C to +70°C</td>
<td>8 Plastic DIP</td>
</tr>
<tr>
<td>MAX735CSA</td>
<td>0°C to +70°C</td>
<td>8 SO</td>
</tr>
<tr>
<td>MAX755C/D</td>
<td>0°C to +70°C</td>
<td>Dice*</td>
</tr>
<tr>
<td>MAX735PA</td>
<td>-40°C to +85°C</td>
<td>8 Plastic DIP</td>
</tr>
<tr>
<td>MAX755ESA</td>
<td>-40°C to +85°C</td>
<td>8 SO</td>
</tr>
<tr>
<td>MAX755MJA</td>
<td>-55°C to +125°C</td>
<td>8 CERDIP**</td>
</tr>
<tr>
<td>MAX755CPA</td>
<td>0°C to +70°C</td>
<td>8 Plastic DIP</td>
</tr>
<tr>
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<td>MAX755MJA</td>
<td>-55°C to +125°C</td>
<td>8 CERDIP**</td>
</tr>
</tbody>
</table>

* Contact factory for dice specifications.
** Contact factory for availability and processing to MIL-STD-883.

**Pin Configuration**

![Pin Configuration Diagram]

Call toll free 1-800-998-8800 for free samples or literature.
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**ABSOLUTE MAXIMUM RATINGS**

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
</table>
| Supply Voltage (V+
+V) to GND) | MAX735 | +7V, -0.3V | | | |
| Switch Voltage (LV+ to V+) | MAX755 (Note 1) | -11.0V, 3V | | | |
| Feedback Voltage (VOUT to GND) | MAX755 | ±25V | | | |
| Auxiliary Input Voltage | MAX755 | ±0.3V to (V+ + 0.3V) | | | |
| Peak Switch Current (L+) | MAX755 | 2.0A | | | |
| Reference Current (IREF) | MAX755 | 2.5mA | | | |
| Continuous Power Dissipation (TA = +70°C) | MAX755 | 471mW | | | |
| Plastic DIP (derate 9.0mW/°C above +70°C) | MAX755 | 727mW | | | |
| SO (derate 5.89mW/°C above +70°C) | MAX755 | 471mW | | | |
| CERDIP (derate 8.00mW/°C above +70°C) | MAX755 | 640mW | | | |

These limits beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**ELECTRICAL CHARACTERISTICS**

(Circuit of Figure 2, V+ = 5V, -5.25V ≤ VOUT ≤ -4.75V, ILOAD = 0mA, TA = TMIN to TMAX, typical values are at TA = +25°C, unless otherwise noted.)

Note 1: Additional: Vin is limited to: Vin ≤ 11.7V – VOUT!

Note 2: MAX755 external/feedback resistor tolerance is 0.1%.

Note 3: Tested at IREF = 8μA for the MAX735, IREF = 125μA for the MAX755.

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**MAX735/MAX755**
-5V/Adjustable, Negative-Output, Inverting, Current-Mode PWM Regulators

Typical Operating Characteristics

**LOAD CURRENT vs. SUPPLY VOLTAGE**

- Ta = +25°C
- Circuit of Fig. 1

**LOAD CURRENT vs. SUPPLY VOLTAGE (MAX735)**

- Ta = +25°C
- Circuit of Fig. 1
- With R1 = 33kΩ, 1%, R4 = 18.2kΩ, 1%

**EFFICIENCY vs. LOAD CURRENT**

- VIn = +6V
- VIn = +4V
- VIn = +3V

**PEAK INDUCTOR CURRENT vs. LOAD CURRENT**

- Ta = +25°C
- Circuit of Fig. 1

**SWITCH ON RESISTANCE vs. SUPPLY VOLTAGE**

- Ta = +25°C
- VIn = -5V

**NO-LOAD SUPPLY CURRENT vs. SUPPLY VOLTAGE (MAX735)**

- Ta = +25°C
- Circuit of Fig. 1

**OSCILLATOR FREQUENCY vs. TEMPERATURE & SUPPLY VOLTAGE**

- VIn = +5V, +4V

**SWITCH-CURRENT LIMIT vs. SOFT-START VOLTAGE**

- Ta = +25°C
- VIn = -5V

**SOFT-START DELAY TIME**

- ILoad = 100mA
- Circuit of Fig. 1
- Ta = +25°C
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Typical Operating Characteristics (continued)

Switching Waveforms

A = SWITCH VOLTAGE (LOW 5V/div)
B = INDUCTOR CURRENT, 500mA/div
C = OUTPUT VOLTAGE RIFFLE, 50mV/div
TIMEBASE = 250μs/div
CIRCUIT OF FIG. 1

Vs = 5V
Ta = +25°C

Load-Transient Response

A = LOAD CURRENT, 0mA TO 200mA
B = OUTPUT VOLTAGE, 50mV/div
TIMEBASE = 10ms/div
CIRCUIT OF FIG. 1

Ve = 5V
Ta = +25°C

Line-Transient Response

A = INPUT VOLTAGE, 5V TO 6V
B = OUTPUT VOLTAGE, 50mV/div
TIMEBASE = 100μs/div
CIRCUIT OF FIG. 1

Iin = 100mA
Ta = +25°C

Pin Description

<table>
<thead>
<tr>
<th>PIN</th>
<th>NAME</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>SHDN</td>
<td>SHUTDOWN Control. V+ = normal operation, GND = shutdown.</td>
</tr>
<tr>
<td>2</td>
<td>VREF</td>
<td>Reference Voltage Output = 1.23V. Supplies up to 125μA for external loads.</td>
</tr>
<tr>
<td>3</td>
<td>SS</td>
<td>Soft-Start</td>
</tr>
<tr>
<td>4</td>
<td>CC</td>
<td>Compensation Input of the error amplifier and feedback summing node.</td>
</tr>
<tr>
<td>5</td>
<td>VOUT</td>
<td>Output Voltage feedback terminal (actually an input); connected to internal resistors (MAX735). Also provides MOSFET driver bias.</td>
</tr>
<tr>
<td>6</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>7</td>
<td>LX</td>
<td>Switch Output - internal P-channel MOSFET drain</td>
</tr>
<tr>
<td>8</td>
<td>V+</td>
<td>Positive Supply-Voltage Input. Bypass with a 1μF ceramic capacitor close to V+ and GND pins. Use additional bypass capacitor as shown in Figures 1, 2, and 3.</td>
</tr>
</tbody>
</table>
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**Detailed Description**

**Operating Principle**

The MAX735/MAX755s are monolithic CMOS ICs containing a current-mode PWM controller and a 2A P-channel power MOSFET. Current-mode control provides excellent line transient response, inherent overload protection, and excellent AC stability. The switch transistor is a current-sensing MOSFET that splits off a fraction of the total source current for current-limit detection.

**Basic Application Circuits**

The three basic application circuits shown are simple designs using standard, off-the-shelf components. Figure 1’s circuit uses tantalum surface-mount capacitors and a surface-mount inductor, minimizing board space and allowing for wide-temperature operation. The low equivalent series resistance (ESR) of the tantalum capacitors (typically 70mΩ at +25°C and 140mΩ at -55°C) makes for a quiet output (see Switching Waveforms in the Typical Operating Characteristics).
-5V/Adjustable, Negative-Output, Inverting, Current-Mode PWM Regulators

MAX735/MAX755

Figure 4. Detailed Block Diagram

Figure 2’s circuit provides a through-hole solution for commercial-temperature operation. The capacitors are radial-lead aluminum electrolytics with an ESR of approximately 100mΩ at +25°C. These and other standard aluminum electrolytic capacitors have an ESR 100 times greater at -55°C than at +25°C, so they are not recommended for operation below 0°C. Since output voltage ripple is proportional to the ESR of the output filter capacitor, the ripple with standard aluminum electrolytic capacitors is 1.4 times that associated with tantalum capacitors. Refer to Figure 3 for a wide-temperature, through-hole solution. The capacitors are organic semiconductor (Os-Con) aluminum electrolytics, which exhibit low ESR over a wide temperature range (typically 30mΩ at +25°C and -55°C).

Table 1 lists component suppliers for the circuits discussed above.

If the load current is limited to 100mA, R1, R2, and C3 (Figures 1-3) may be omitted. The 1.0µF V+ bypass capacitor must be placed as close as possible to pins 6 and 8.

Output-Ripple Filtering

An optional lowpass pi-filter (Figures 1-3) can be added to the output to reduce output ripple to about 5mVp-p. The cutoff frequency of the filter shown is 21kHz. Since the filter inductor is in series with the circuit output, its resistance should be minimized to avoid excessive voltage drop. Note that the feedback must be taken before the filter, not after the filter.
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**Soft-Start Buffer**

The voltage applied to the Soft-Start (SS) input determines the peak switch-current limit (see Soft-Start Delay Time graph in Typical Operating Characteristics). A capacitor attached to SS ensures an orderly power-up sequence by gradually increasing the current limit. SS is pulled up to VREF internally through a 1.2MΩ resistor. The maximum current limit can be fixed externally at a lower than normal value by clamping the SS voltage to a voltage less than VREF. An SS cycle is initiated whenever either an undervoltage lockout (MAX735 only) or overcurrent fault condition triggers an internal transistor to discharge the SS capacitor to ground. Note that the SS capacitor should be at least 10nF for the overcurrent limit to function properly.

**Undervoltage Lockout**

The MAX735 operates for supply voltages greater than 3.7V typ (4V guaranteed), with 0.25V of hysteresis. Internal control logic holds the output power MOSFET off until the supply rises above the undervoltage threshold, at which time a soft-start cycle begins.

The MAX755 operates with supply voltages greater than +2.7V. It does not have the undervoltage lockout feature of the MAX735. The output is limited to \( \text{IVout} \leq 11.7V \) - VIN.

**Inductor Selection**

The MAX735 and MAX755 operate with a standard 10μH inductor for the entire range of supply voltages and load currents. The inductor must have a saturation (incremental) current rating greater than the peak switch current obtained from the Peak Inductor Current vs Load Current graph under Typical Operating Characteristics.

**Output Adjustment - MAX755**

The output voltage for the MAX755 is set by two resistors, R3 and R4, which form a voltage divider between the output, CC pin, and VREF pin. The regulator adjusts the output voltage so the voltage at CC's GND. R4 can be any value from 10kΩ to 20kΩ. R4 is given by the following formula:

\[
\frac{\text{IVout}}{R3} = 1.23V\]

The output is limited to \( \text{IVout} \leq 11.7V \) - VIN.

---

**Table 1. Component Suppliers**

<table>
<thead>
<tr>
<th>PRODUCTION METHOD</th>
<th>INDUCTORS</th>
<th>CAPACITORS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Surface Mount</td>
<td>Sumida</td>
<td>Matsuo 267 series</td>
</tr>
<tr>
<td>Miniature</td>
<td>Sumida</td>
<td>Sanyo  Cis-Con series low-ESR organic semiconductor</td>
</tr>
<tr>
<td>Through Hole</td>
<td>RCH855-100M (10μH)</td>
<td>low-ESR electrolyics</td>
</tr>
<tr>
<td>Low-Cost</td>
<td>Renco RL 1284 (10μH)</td>
<td>United Chemicon LUF series</td>
</tr>
</tbody>
</table>

Matsuo USA (714) 965-2491 FAX (714) 960-6492
Matsuo Japan (03) 353-2371
Nichicon (708) 943-7500 FAX (708) 943-2796
Renco (516) 566-3066 FAX (516) 566-3562
Sanyo Cis-Con USA (619) 661-6322
Sanyo Cis-Con Japan (0720) 70-1050 FAX (0720) 70-1174
Sumida USA (708) 966-0666
Sumida Japan (03) 3607-5111 FAX (03) 3607-5428
United Chemicon (708) 896-2000 FAX (708) 640-6311

**Printed Circuit Layout and Grounding**

Good layout and grounding practices will ensure low-noise, jitter-free operation. Minimize wire lengths in the high-current paths, especially the distance between the inductor and the return leads of the filter and bypass capacitors (C1 and C2). These high-current ground connections should be brought to a single common point (a "star" ground). Place a low-ESR bypass capacitor directly at V+ and GND. The use of sockets or wire-wrap boards is not recommended.
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Chip Topography

MAX735/MAX755

Note: TRANSISTOR COUNT: 274
CONNECT SUBSTRATE TO V+

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