**General Description**

The MAX5932 is a fully integrated hot-swap controller for +9V to +80V positive supply rails. The MAX5932 allows for the safe insertion and removal of circuit cards into a live backplane without causing glitches on the backplane power-supply rail. This device is pin and function compatible to LT1641-1. The MAX5932 features a programmable foldback-current limit. If the device remains in current limit for more than a programmable time, the external n-channel MOSFET latches off. Other features include a programmable undervoltage lockout and a programmable output-voltage slew rate through an external n-channel MOSFET.

The MAX5932 provides a power-good output (PWRGD) to indicate the status of the output voltage. For a variety of PWRGD/PWRGD latch/autoretry-fault management, autoretry duty-cycle options, refer to the MAX5933 and MAX5934 data sheets.

The MAX5932 operates in the -40°C to +85°C extended temperature range. This device is available in an 8-pin SO package.

**Features**

- Pin and Function Compatible with LT1641-1
- Provides Safe Hot Swap for +9V to +80V Power Supplies
- Safe Board Insertion and Removal from Live Backplanes
- Active-High Power-Good Output (PWRGD)
- Programmable Foldback-Current Limiting
- High-Side Drive for an External N-Channel MOSFET
- Undervoltage Lockout (UVLO)
- Overvoltage Protection
- Latched Fault Management
- User-Programmable Supply Voltage Power-Up Rate

**Applications**

- Hot Board Insertion
- Electronic Circuit Breaker
- Industrial High-Side Switch/Circuit Breaker
- Network Routers and Switches
- 24V/48V Industrial/Alarm Systems

**Ordering Information**

<table>
<thead>
<tr>
<th>PART</th>
<th>TEMP RANGE</th>
<th>PIN-PACKAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAX5932ESA</td>
<td>-40°C to +85°C</td>
<td>8 SO</td>
</tr>
</tbody>
</table>

**Pin Configuration**

*Typical Application Circuit appears at end of data sheet.*
**Positive High-Voltage, Hot-Swap Controller**

**ABSOLUTE MAXIMUM RATINGS**

(Voltages referenced to GND)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage Range</td>
<td>VCC</td>
<td>-0.3V to +85V</td>
<td>9</td>
<td>80</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Supply Current</td>
<td>ICC</td>
<td>VON = 3V, VCC = 80V</td>
<td>1.4</td>
<td>3.5</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>VCC Undervoltage Lockout</td>
<td>VLKO</td>
<td>VCC low-to-high transition</td>
<td>7.5</td>
<td>8.3</td>
<td>8.8</td>
<td>V</td>
</tr>
<tr>
<td>VCC Undervoltage Lockout Hysteresis</td>
<td>VLKOHYST</td>
<td>-0.3V to +95V</td>
<td>0.4</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>FB High-Voltage Threshold</td>
<td>VFBH</td>
<td>FB low-to-high transition</td>
<td>1.280</td>
<td>1.313</td>
<td>1.345</td>
<td>V</td>
</tr>
<tr>
<td>FB Low-Voltage Threshold</td>
<td>VFBFL</td>
<td>FB high-to-low transition</td>
<td>1.221</td>
<td>1.233</td>
<td>1.245</td>
<td>V</td>
</tr>
<tr>
<td>FB Hysteresis</td>
<td>VFBHYST</td>
<td>-0.3V to +95V</td>
<td>80</td>
<td>mV</td>
<td></td>
<td></td>
</tr>
<tr>
<td>FB Input Bias Current</td>
<td>IINFB</td>
<td>VFB = 0V</td>
<td>-1</td>
<td>+1</td>
<td>µA</td>
<td></td>
</tr>
<tr>
<td>FB Threshold Line Regulation</td>
<td>ΔVF</td>
<td>9V ≤ VCC ≤ 80V, ON = 0V, TA = 0°C to +70°C</td>
<td>0.05</td>
<td>mV/V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>SENSE Trip Voltage (VCC - VSENSE)</td>
<td>VSENSETRI</td>
<td>VFB = 0V, TA = 0°C to +70°C</td>
<td>8</td>
<td>12</td>
<td>17</td>
<td>mV</td>
</tr>
<tr>
<td>GATE Pullup Current</td>
<td>IGATEUP</td>
<td>Charge pump on, VGATE = 7V</td>
<td>-5</td>
<td>-10</td>
<td>-20</td>
<td>µA</td>
</tr>
<tr>
<td>GATE Pulldown Current</td>
<td>IGATEDN</td>
<td>Any fault condition, VGATE = 2V</td>
<td>35</td>
<td>70</td>
<td>100</td>
<td>mA</td>
</tr>
<tr>
<td>External N-Channel Gate Drive</td>
<td>ΔVGATE</td>
<td>VGATE - VCC</td>
<td>4.5</td>
<td>6.2</td>
<td>18</td>
<td>V</td>
</tr>
<tr>
<td>TIMER Pulldown Current</td>
<td>ITIMERD</td>
<td>VTIMER = 1V</td>
<td>1.5</td>
<td>3</td>
<td>4.5</td>
<td>µA</td>
</tr>
<tr>
<td>ON Logic-High Threshold</td>
<td>VONH</td>
<td>ON low-to-high transition</td>
<td>1.280</td>
<td>1.313</td>
<td>1.355</td>
<td>V</td>
</tr>
<tr>
<td>ON Logic-Low Threshold</td>
<td>VONL</td>
<td>ON high-to-low transition</td>
<td>1.221</td>
<td>1.233</td>
<td>1.245</td>
<td>V</td>
</tr>
<tr>
<td>ON Hysteresis</td>
<td>VONHYST</td>
<td>-0.3V to +95V</td>
<td>80</td>
<td>mV</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ON Input Bias Current</td>
<td>IINON</td>
<td>VON = 0V</td>
<td>-1</td>
<td>+1</td>
<td>µA</td>
<td></td>
</tr>
<tr>
<td>PWRGD Leakage Current</td>
<td>IOH</td>
<td>VpwrGD = 80V</td>
<td>10</td>
<td>µA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>PWRGD Output Low Voltage</td>
<td>VOL</td>
<td>IO = 2mA</td>
<td>0.4</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>IO = 4mA</td>
<td>2.5</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SENSE Input Bias Current</td>
<td>ISENSE</td>
<td>VSENSE = 0V to VCC</td>
<td>-1</td>
<td>+3</td>
<td>µA</td>
<td></td>
</tr>
<tr>
<td>Thermal Shutdown</td>
<td>Temperature rising</td>
<td>150</td>
<td>°C</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Thermal Shutdown Hysteresis</td>
<td>-</td>
<td>20</td>
<td>°C</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Continuous Power Dissipation (TA = +70°C): 8-Pin SO (derate 5.9mW/°C above +70°C) ... 470mW
Operating Temperature Range: -40°C to +85°C
Maximum Junction Temperature: +150°C
Storage Temperature Range: -60°C to +150°C
ESD Rating (Human Body Model): ... 2000V
Lead Temperature (soldering, 10s): ... +300°C

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**ELECTRICAL CHARACTERISTICS**

(VCC = +24V, GND = 0V, TA = -40°C to +85°C, unless otherwise noted. Typical values are at TA = +25°C.) (Note 1)
ELECTRICAL CHARACTERISTICS (continued)

\( V_{CC} = +24V, \ GND = 0V, \ TA = -40^\circ C \) to \( +85^\circ C \), unless otherwise noted. Typical values are at \( TA = +25^\circ C \). (Note 1)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SYMBOL</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>ON Low-to-GATE Low Propagation Delay</td>
<td>( t_{PHLON} )</td>
<td>( CGATE = 0, ) Figures 1, 2</td>
<td>6</td>
<td>6</td>
<td>6</td>
<td>( \mu s )</td>
</tr>
<tr>
<td>ON High-to-GATE High Propagation Delay</td>
<td>( t_{PLHON} )</td>
<td>( CGATE = 0, ) Figures 1, 2</td>
<td>1.7</td>
<td>1.6</td>
<td>1.7</td>
<td>( \mu s )</td>
</tr>
<tr>
<td>FB Low-to-PWRGD Low Propagation Delay</td>
<td>( t_{PHLFB} )</td>
<td>Figures 1, 3</td>
<td>3.2</td>
<td>3.2</td>
<td>3.2</td>
<td>( \mu s )</td>
</tr>
<tr>
<td>FB High-to-PWRGD High Propagation Delay</td>
<td>( t_{PLHFB} )</td>
<td>Figures 1, 3</td>
<td>1.5</td>
<td>1.5</td>
<td>1.5</td>
<td>( \mu s )</td>
</tr>
<tr>
<td>( (V_{CC} - V_{SENSE}) ) High-to-GATE Low Propagation Delay</td>
<td>( t_{PHLSENSE} )</td>
<td>( TA = +25^\circ C, \ CGATE = 0, ) Figures 1, 4</td>
<td>0.5</td>
<td>0.5</td>
<td>2</td>
<td>( \mu s )</td>
</tr>
</tbody>
</table>

Note 1: All currents into the device are positive and all currents out of the device are negative. All voltages are referenced to ground, unless noted otherwise.
Positive High-Voltage, Hot-Swap Controller

Test Circuit and Timing Diagrams

Figure 1. Test Circuit

Figure 2. ON to GATE Timing

Figure 3. FB to PWRGD Timing

Figure 4. SENSE to GATE Timing
Positive High-Voltage, Hot-Swap Controller

Typical Operating Characteristics

\((V_{CC} = +48\text{V}, \, T_{A} = +25\degree\text{C}, \, \text{unless otherwise noted.})\)
Positive High-Voltage, Hot-Swap Controller

Typical Operating Characteristics (continued)

(V<sub>CC</sub> = +48V, T<sub>A</sub> = +25°C, unless otherwise noted.)

![Graphs showing various electrical characteristics of the MAX5932 controller.](image-url)
## Pin Description

<table>
<thead>
<tr>
<th>PIN</th>
<th>NAME</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>ON</td>
<td>On/Off Control Input. ON is used to implement the undervoltage lockout threshold and resets the part after a fault condition (see the Detailed Description section).</td>
</tr>
<tr>
<td>2</td>
<td>FB</td>
<td>Power-Good Comparator Input. Connect a resistive divider from output to FB to GND to monitor the output voltage (see the Power-Good Detection section). FB is also used as a feedback for the current-limit foldback function.</td>
</tr>
<tr>
<td>3</td>
<td>PWRGD</td>
<td>Open-Drain Power-Good Output. PWRGD is high when VFB is higher than VFBH. PWRGD is low when VFB is lower than VFBL.</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>5</td>
<td>TIMER</td>
<td>Timing Input. Connect a capacitor from TIMER to GND to program the maximum time the part is allowed to remain in current limit (see the TIMER section).</td>
</tr>
<tr>
<td>6</td>
<td>GATE</td>
<td>Gate-Drive Output. The high-side gate drive for the external n-channel MOSFET (see the GATE section).</td>
</tr>
<tr>
<td>7</td>
<td>SENSE</td>
<td>Current-Sense Input. Connect a sense resistor from VCC to SENSE and the drain of the external n-channel MOSFET.</td>
</tr>
<tr>
<td>8</td>
<td>VCC</td>
<td>Power-Supply Input. Bypass VCC to GND with a 0.1µF capacitor. Input voltage range is from +9V to +80V.</td>
</tr>
</tbody>
</table>
Positive High-Voltage, Hot-Swap Controller

Functional Diagram
**Detailed Description**

The MAX5932 is a fully integrated hot-swap controller for positive supply rails. The device allows for the safe insertion and removal of circuit cards into live backplanes without causing glitches on the backplane power-supply rail. During startup the MAX5932 acts as a current regulator using an external sense resistor and MOSFET to limit the amount of current drawn by the load.

The MAX5932 features latched-off fault management. When an overcurrent or an overtemperature fault occurs, the MAX5932 turns the external MOSFET off and keeps it off. After the fault condition goes away, cycle the power supply or toggle ON low and high again to unlatch the device.

The MAX5932 operates from +9V to +80V supply voltage range and has a default undervoltage lockout (UVLO) set to +8.3V. The UVLO threshold is adjustable using a resistive divider connected from VCC to ON to GND (see R1 and R2 in Figure 5).

The MAX5932 monitors the input voltage, the output voltage, the output current, and the die temperature. This device features a power-good output (PWRGD) to indicate the status of the output voltage by monitoring the voltage at FB (see the Power-Good Detection section).

As shown in Figure 5, a sense resistor is connected between VCC and SENSE to sense the load current. The device regulates the voltage across the sense resistor (VIN - VSENSE) to 47mV when the voltage at FB ≥ 0.5V. The current-limit threshold (VSENSETRI) decreases linearly from 47mV to 12mV as FB decreases from 0.5V to 0V.

An undervoltage fault is detected when ON goes below the threshold, VONL = 1.233V, which causes the voltage at GATE to go low, and results in turning off the MOSFET. To turn the MOSFET on again, ON must pass the VONH = 1.313V threshold.
Applications Information

Hot-Circuit Insertion
When circuit boards are inserted into a live backplane, the supply bypass capacitors on the boards draw high peak currents from the backplane power bus as they charge up. The transient currents can permanently damage the connector pins and glitch the system supply, causing other boards in the system to reset.

Power-Up Sequence
The power supply on a board is controlled by placing an external n-channel MOSFET (Q1) in the power path (Figure 5). Resistor R\textsubscript{SENSE} provides current detection and capacitor C\textsubscript{1} provides control of the GATE slew rate. Resistor R\textsubscript{6} provides current control-loop compensation while R\textsubscript{5} prevents high-frequency oscillations in Q1. Resistors R\textsubscript{1} and R\textsubscript{2} provide undervoltage sensing.

After the power pins first make contact, transistor Q1 is turned off. When the voltage at ON exceeds the turn-on threshold voltage, the voltage on V\textsubscript{CC} exceeds the undervoltage lockout threshold, and the voltage on TIMER is less than 1.233V, transistor Q1 turns on (Figure 6).

The voltage at GATE rises with a slope equal to 10µA/C\textsubscript{1} and the supply inrush current is set at:

\[ I\text{INRUSH} = C_L \times 10\mu A/C_1 \]

When the voltage across the current-sense resistor R\textsubscript{SENSE} reaches V\textsubscript{SENSE\textsubscript{TRIP}}, then the inrush current is limited by the internal current-limit circuitry that adjusts the voltage on GATE to maintain a constant voltage across the sense resistor.

Once the voltage at the output has reached its final value, as sensed by resistors R\textsubscript{3} and R\textsubscript{4}, PWRGD goes high.

Short-Circuit Protection
The MAX5932 features a programmable foldback current limit with an electronic circuit breaker that protects against short circuits or excessive supply currents. The current limit is set by placing a sense resistor between V\textsubscript{CC} (pin 8) and SENSE (pin 7).

To prevent excessive power dissipation in the pass transistor and to prevent voltage spikes on the input supply during short-circuit conditions at the output, the current folds back as a function of the output voltage that is sensed at FB (Figure 7).

When the voltage at FB is 0V, the current-limit circuit drives GATE to force a constant 12mV drop across the sense resistor. As the output voltage at FB increases, the voltage across the sense resistor increases until FB reaches 0.5V, at the point that the voltage across the sense resistor is held constant at 47mV.

The maximum current limit is calculated as:

\[ I\text{LIMIT} = 47mV/R\text{SENSE} \]

For a 0.025Ω sense resistor, the current limit is set at 1.88A and folds back to 480mA when the output is shorted to ground.

The MAX5932 also features a variable overcurrent response time. The time required to regulate Q1’s drain current depends on:

- Q1’s input capacitance.
- GATE capacitor C\textsubscript{1} and compensation resistor R\textsubscript{6}.
- The internal delay from SENSE to GATE.

Figure 8 shows the delay from a voltage step at SENSE until GATE voltage starts falling, as a function of overdrive.

![Figure 6. Power-Up Waveforms](image)

![Figure 7. Current-Limit Sense Voltage vs. Feedback Voltage](image)
**Positive High-Voltage, Hot-Swap Controller**

**TIMER**

TIMER provides a method for programming the maximum time the device is allowed to operate in current limit. When the current-limit circuitry is not active, TIMER is pulled to GND by a 3μA current source. After the current-limit circuit becomes active, an 80μA pullup current source is connected to TIMER and the voltage rises with a slope equal to 77μA/CTIMER as long as the current-limit circuit remains active. Once the desired maximum current-limit time is chosen, the capacitor value is calculated using the following equations:

\[ C(nF) = 65 \times t(ms) \]

or

\[ T_{\text{LIMIT}} = \left( \frac{C_{\text{TIMER}}}{80\mu A} \right) \times 1.233V \]

When the current-limit circuit turns off, TIMER is discharged to GND by the 3μA current source. Whenever TIMER reaches 1.233V, the internal fault latch is set. GATE is immediately pulled to GND and TIMER is pulled back to GND by the 3μA current source. When TIMER falls below 0.5V, ON is pulsed low to reset the internal fault latch.

The waveform in Figure 9 shows how the output latches off following a short circuit. The drop across the sense resistor is held at 12mV as the timer ramps up. Since the output did not rise, FB remains below 0.5V and the circuit latches off. For Figure 9, \( C_T = 100nF \).

**Undervoltage and Overvoltage Detection**

ON can be used to detect an undervoltage condition at the power-supply input. ON is internally connected to an analog comparator with 80mV of hysteresis. If ON falls below its threshold voltage (1.233V), GATE is pulled low and is held low until ON is high again.

**Power-Good Detection**

The MAX5932 includes a comparator for monitoring the output voltage. The noninverting input (FB) is compared against an internal 1.233V precision reference and exhibits 80mV hysteresis. The comparator’s output (PWRGD) is an open drain one capable of operating from a pullup as high as 80V. The PWRGD can be used to directly enable/disable a power module with an active-high enable input. Figure 12 shows how to use PWRGD to control an active-low enable-input power module. Signal inversion is accomplished by transistor Q2 and R7 or use MAX5933.

**Supply Transient Protection**

The MAX5932 is 100% tested and guaranteed to be safe from damage with supply voltages up to 80V. However, spikes above 85V may damage the device. During a short-circuit condition, the large change in currents flowing through the power-supply traces can cause inductive voltage spikes that could exceed 85V. To minimize the spikes, the power-trace parasitic inductance should be minimized by using wider traces or heavier trace plating and a 0.1μF bypass capacitor placed between VCC and GND. A transient voltage suppressor (TVS) at the input can also prevent damage from voltage surges.
GATE Voltage

A curve of Gate Drive vs. \( V_{CC} \) is shown in Figure 13. GATE is clamped to a maximum voltage of 18V above the input voltage. At a minimum input-supply voltage of 9V, the minimum gate-drive voltage is 4.5V. When the input-supply voltage is higher than 20V, the gate-drive voltage is at least 10V and a standard n-channel MOSFET can be used. In applications over a 9V to 20V range, a logic-level n-FET must be used with a proper protection Zener diode between its gate and source (as D1 shown in Figure 5).

Thermal Shutdown

If the MAX5932 die temperature reaches +150°C, an overtemperature fault is generated. As a result, GATE goes low and turns the external MOSFET off. The MAX5932 die temperature must cool down below +130°C before the overtemperature fault condition is removed.
Positive High-Voltage, Hot-Swap Controller

Layout Considerations

To achieve accurate current sensing, a Kelvin connection is recommended. The minimum trace width for 1oz copper foil is 0.02 in per amplifier to make sure the trace stays at a reasonable temperature. Using 0.03 in per amplifier or wider is recommended. Note that 1oz copper exhibits a sheet resistance of about 530 µΩ/square. Small resistances add up quickly in high-current applications. To improve noise immunity, connect the resistor-divider to ON close to the device and keep traces to VCC and GND short. A 0.1 µF capacitor from ON to GND also helps reject induced noise. Figure 14 shows a layout that addresses these issues.

External MOSFET must be thermally coupled to the MAX5932 to ensure proper thermal shutdown operation (see Figure 14).
Positive High-Voltage, Hot-Swap Controller

Chip Information

TRANSISTOR COUNT: 1573
PROCESS: BiCMOS

Figure 14. Recommended Layout for R1, R2, and RSENSE

Typical Application Circuit
Positive High-Voltage, Hot-Swap Controller

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)

NOTES:
1. D & E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15mm (.006").
3. LEADS TO BE COPLANAR WITHIN 0.10mm (.004").
4. CONTROLLING DIMENSIONS: MILLIMETERS.
5. MEETS JEDEC MS012.
6. N = NUMBER OF PINS.

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Maxim Integrated Products, 120 San Gabriel Drive, Sunnyvale, CA 94086 408-737-7600

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