**General Description**

The MAX3864 is a transimpedance preamplifier for applications in SDH/SONET systems operating up to 2.5Gbps. It features 490nA (typ) input-referred noise, 2.0GHz bandwidth, and 2mA input overload.

The MAX3864 operates from a single +3.0V to +5.5V supply. It includes an integrated low-frequency compensation capacitor, as well as a filter connection that provides positive bias through a 750Ω resistor to VCC. These features save external components, simplifying design and assembly into a TO-46 header with a photodiode.

The MAX3864 has a typical optical dynamic range of -24dBm to 0dBm using a PIN photodetector.

**Features**

- 490nA (typ) Input-Referred Noise
- 2000MHz Bandwidth
- 2mA Input Overload
- 100Ω Differential Output Impedance
- 112mW Power Dissipation at +3.3V
- Integrated Filter Resistor
- CML Outputs
- Single +3.0V to +5.5V Supply Voltage

**Applications**

- SDH/SONET Transmission Systems
- PIN Preamplifier Receivers
- APD Preamplifier Receivers
- 2.5Gbps ATM Receivers
- Regenerators for SDH/SONET

**Ordering Information**

<table>
<thead>
<tr>
<th>PART</th>
<th>TEMP RANGE</th>
<th>PIN-PACKAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAX3864ESA</td>
<td>-40°C to +85°C</td>
<td>8 SO</td>
</tr>
<tr>
<td>MAX3864E/D</td>
<td>-40°C to +85°C</td>
<td>Dice*</td>
</tr>
</tbody>
</table>

* Dice are designed to operate with junction temperatures of -40°C to +140°C but are tested and guaranteed only at TA = +25°C.

**Typical Application Circuit**

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim’s website at www.maxim-ic.com.
2.5Gbps, +3V to +5.5V, Wide Dynamic Range Transimpedance Preamplifier

ABSOLUTE MAXIMUM RATINGS

Supply Voltage (Vcc - GND) ......................-0.5V to +6.0V
IN Current.........................................-4mA to +4mA
FILTER Current....................................-8mA to +8mA
Voltages at OUT+, OUT- ....................(VCC - 1.5V) to (VCC + 0.5V)
Continuous Power Dissipation (TA = +85°C)
8-Pin SO package (derate 6.7mW/°C above +85°C) ..436mW

ABSOLUTE MAXIMUM RATINGS

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(VCC = +3.0V to +5.5V, 100Ω load between OUT+ and OUT-, 0.1µF coupling capacitors on OUT+ and OUT-, TA = -40°C to +85°C, unless otherwise noted. Typical values are at +3.3V, source capacitance = 0.85pF, and TA = +25°C.) (Note 1)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Bias Voltage</td>
<td>0.66</td>
<td>0.83</td>
<td>0.99</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Supply Current</td>
<td>34</td>
<td>63</td>
<td>mA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Transimpedance</td>
<td>Differential, measured with 40µA_P input</td>
<td>2100</td>
<td>2750</td>
<td>3400</td>
<td>Ω</td>
</tr>
<tr>
<td>Output Impedance</td>
<td>Single ended (per side)</td>
<td>48</td>
<td>50</td>
<td>52</td>
<td>Ω</td>
</tr>
<tr>
<td>Maximum Differential Output Voltage</td>
<td>Input = 2mAP-P with 100Ω differential output termination</td>
<td>220</td>
<td>380</td>
<td>575</td>
<td>mVP-P</td>
</tr>
<tr>
<td>Filter Resistor</td>
<td>600</td>
<td>750</td>
<td>930</td>
<td>Ω</td>
<td></td>
</tr>
<tr>
<td>AC Input Overload</td>
<td>2</td>
<td>mAP-P</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>DC Input Overload</td>
<td>1</td>
<td>mA</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input-Referred RMS Noise</td>
<td>490</td>
<td>668</td>
<td>nA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input-Referred Noise Density</td>
<td>Bandwidth = 2.0GHz (Note 2)</td>
<td>11</td>
<td>pA/√Hz</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Small-Signal Bandwidth</td>
<td>1525</td>
<td>2000</td>
<td>MHz</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Low-Frequency Cutoff</td>
<td>-3dB, input ≤ 20µADC</td>
<td>30</td>
<td>kHz</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Transimpedance Linear Range</td>
<td>Gain at 40µAP-P is within 5% of the small-signal gain</td>
<td>40</td>
<td>μAP-P</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Deterministic Jitter</td>
<td>3.13V &lt; VCC &lt; 5.5V (Note 3)</td>
<td>24</td>
<td>67</td>
<td>ps</td>
<td></td>
</tr>
<tr>
<td></td>
<td>3.0V ≤ VCC ≤ 3.13V (Note 3)</td>
<td>24</td>
<td>77</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Power-Supply Rejection Ratio (PSRR)</td>
<td>Output referred, f &lt; 2MHz, PSRR = -20log(ΔVOUT/ΔVCC)</td>
<td>50</td>
<td>dB</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Note 1: Source capacitance represents the total capacitance at the IN pin during characterization of noise and bandwidth parameters. Noise and bandwidth will be affected by the source capacitance. See the Typical Operating Characteristics for more information.

Note 2: Input-referred noise is calculated as (RMS output noise) / (Gain at f = 10MHz). Noise density is (Input-Referred Noise) / (Bandwidth)^1/2. No external filters are used for the noise measurements.

Note 3: Deterministic jitter is defined as the arithmetic sum of pulse-width distortion and pattern dependent jitter measured with a repeating 20-bit pattern of 0011110101100000101 (K28.5). See Typical Operating Characteristics.
2.5Gbps, +3V to +5.5V, Wide Dynamic Range Transimpedance Preamplifier

Typical Operating Characteristics

(VCC = 3.3V, TA = +25°C and MAX3864 EV kit source capacitance = 0.85pF, unless otherwise noted).
The MAX3864 transimpedance amplifier is designed for 2.5Gbps fiber optic applications. As shown in Figure 1, the MAX3864 comprises a transimpedance amplifier, a voltage amplifier, an output buffer, an output filter, and a DC cancellation circuit.

**Transimpedance Amplifier**

The signal current at the input flows into the summing node of a high-gain amplifier. Shunt feedback through RF converts this current to a voltage. Schottky diodes clamp the output voltage for large input currents (Figure 2).

**Voltage Amplifier**

The voltage amplifier converts single-ended signals to differential signals and introduces a voltage gain.

**Output Buffer**

The output buffer provides a back-terminated voltage output. The buffer is designed to drive a 100Ω differential load between OUT+ and OUT-. The output voltage is divided between internal 50Ω load resistors and the external load resistor. In the typical operating circuit, this creates a voltage-divider with a ratio of 1/2. The MAX3864 can also be terminated with higher output impedances, which increases gain and output voltage swings.
**2.5Gbps, +3V to +5.5V, Wide Dynamic Range Transimpedance Preamplifier**

For optimum supply-noise rejection, the MAX3864 should be terminated with a differential load. If a single-ended output is required, the unused output should be terminated with 50Ω to VCC. The MAX3864 will not drive a DC-coupled, 50Ω grounded load.

**Output Filter**

The MAX3864 includes a one-pole lowpass filter that limits the circuit bandwidth and improves noise performance.

**DC Cancellation Circuit**

The DC cancellation circuit uses low-frequency feedback to remove the DC component of the input signal (Figure 3). This feature centers the input signal within the transimpedance amplifier’s linear range, thereby reducing pulse-width distortion (PWD) on large input signals.

The DC cancellation circuit is internally compensated and therefore does not require external capacitors. This circuit minimizes PWD for data sequences that exhibit a 50% duty cycle and mark density. A duty cycle or mark density significantly different from 50% causes the MAX3864 to generate PWD.

DC cancellation current is drawn from the input and creates noise. For low-level signals with little or no DC component, this is not a problem. Amplifier noise will increase for signals with significant DC component (see *Typical Operating Characteristics*).
Many of the MAX3864 specifications relate to the input signal amplitude. When working with fiber optic receivers, the input is usually expressed in terms of average optical power and extinction ratio. Figure 4 shows relations that are helpful for converting optical power to input signal when designing with the MAX3864.

Optical sensitivity calculations

The MAX3864 input-referred RMS noise current ($I_N$) generally determines the receiver sensitivity. To obtain a system bit-error rate (BER) of 1E-10, the minimum signal-to-noise ratio (SNR) is 12.7. The input sensitivity, expressed in average power, can be estimated as:

$$\text{Sensitivity} = 10 \log \left( \frac{\text{SNR} \times I_N (r_e + 1)}{2p(r_e - 1) \times 1000} \right) \text{dBm}$$

where $\rho$ is the photodiode responsivity, including fiber-to-photodiode coupling efficiency in A/W and $I_N$ in µA. For example, if SNR = 12.7, $I_N = 0.490$µA, $r_e = 10$, and $\rho = 1$, then sensitivity is -24dBm.

Input Optical Overload

The overload is the largest input that the MAX3864 accepts while meeting deterministic jitter specifications. The optical overload can be estimated in terms of average power with the following equation (assumes $r_e = \infty$):

$$\text{Overload} = 10 \log \left( \frac{2mAp - \rho \times 1000}{2p} \right) \text{dBm}$$

Optical Linear Range

The MAX3864 has high gain, which limits the outputs when the input signal exceeds 40µAp-p. The MAX3864 operates in a linear range for inputs not exceeding:

$$\text{Linear Range} = 10 \log \left( \frac{40\mu Ap - p(r_e + 1) \times 1000}{2p(r_e - 1)} \right) \text{dBm}$$

Layout Considerations

Use good high-frequency design and layout techniques. The use of a multilayer circuit board with separate ground and power planes is recommended. Connect the GND pins to the ground plane with the shortest possible traces. Noise performance and bandwidth will be adversely affected by capacitance at the IN pin. Minimize capacitance on this pin, and select a low-capacitance photodiode. Assembling the MAX3864 in die form using chip and wire technology provides the best possible performance. Figure 5 shows the recommended layout for a TO header.

The SO package version of the MAX3864 is offered as an easy way to characterize the circuit and to become familiar with the circuit’s operation, but it does not offer optimum performance. When using the SO version of the MAX3864, the package capacitance adds approximately 0.3pF at the input. The PC board between the MAX3864 input and the photodiode also adds parasitic capacitance. Keep the input line short, and remove power and ground planes beneath it.

Connect GND as close to the AC ground of the photodetector diode as possible. The photodetector AC ground is usually the ground of the filter capacitor from the photodetector cathode. The total loop (from GND, through the bypass capacitor and the diode, and back to IN) should be no more than approximately 1/5th of a wavelength.
**Photodiode Filter**

Supply voltage noise at the photodiode cathode produces a current \( I = \frac{C_{PD} \Delta V}{\Delta t} \), which reduces the receiver sensitivity (\( C_{PD} \) is the photodiode capacitance). The filter resistor of the MAX3864, combined with an external capacitor, can be used to reduce this noise (see the Typical Application Circuit). Current generated by supply noise voltage is divided between \( C_{FILTER} \) and \( C_{PD} \). The input noise current due to supply noise is (assuming the filter capacitor is much larger than the photodiode capacitance):

\[
I_{\text{NOISE}} = \left( \frac{V_{\text{NOISE}}}{R_{\text{FILTER}}} \right) \left( \frac{C_{PD}}{C_{FILTER}} \right)
\]

If the amount of tolerable noise is known, the filter capacitor can be easily selected:

\[
C_{\text{FILTER}} = \left( \frac{V_{\text{NOISE}}}{R_{\text{FILTER}}} \right) \left( \frac{C_{PD}}{I_{\text{NOISE}}} \right)
\]

For example, with a maximum noise voltage equal to 100mV-P-P, \( C_{PD} = 0.85\text{pF} \), \( R_{\text{FILTER}} = 750\Omega \), and \( I_{\text{NOISE}} \) selected to be 250nA (half of the MAX3864’s input noise):

\[
C_{\text{FILTER}} = \left( \frac{100\text{mV}}{750\Omega} \right) \left( \frac{0.85\text{pF}}{250\text{nA}} \right) = 453\text{pF}
\]

**Wire Bonding**

For high current density and reliable operation, the MAX3864 uses gold metalization. Connections to the die should be made with gold wire only, using ball-bonding. Wedge bonding is not recommended. Die thickness is typically 15mils (0.375mm).
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Figure 7. Equivalent Output Circuit
2.5Gbps, +3V to +5.5V, Wide Dynamic Range Transimpedance Preamplifier

Chip Topography

Chip Information

TRANSISTOR COUNT: 320
PROCESS: BIPOLAR (SILICON GERMANIUM)
2.5Gbps, +3V to +5.5V, Wide Dynamic Range Transimpedance Preamplifier

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)

NOTES:
1. D & E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15mm (.006").
3. LEADS TO BE COPLANAR WITHIN 0.10mm (.004").
4. CONTROLLING DIMENSION: MILLIMETERS.
5. MEETS JEDEC MS012.
6. N = NUMBER OF PINS.

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