

General Description

The MAX3157 is a high CMRR RS-485/RS-422 data-communications interface providing ±50V isolation in a hybrid microcircuit. A single +5V supply on the logic side powers both sides of the interface, with external ±50V capacitors transferring power from the logic side to the isolated side. Each MAX3157 contains one transmitter and one receiver and is guaranteed to operate at data rates up to 250kbps. Drivers are short-circuit current limited and protected against excessive power dissipation by thermal shutdown circuitry that places the driver outputs into a high-impedance state. The receiver input has a fail-safe feature that guarantees a logic high receiver output if the inputs are open, shorted, or connected to a terminated transmission line with all drivers disabled.

The MAX3157 typically draws 25mA of supply current when unloaded or when fully loaded with the driver disabled. Supply current drops to 25µA when the device is placed in shutdown mode. The device is pin selectable between half- and full-duplex mode and also features independently programmable receiver and transmitter output phase through separate pins. The MAX3157 is a low-cost replacement for opto-isolated transceivers. For fully isolated RS-485/RS-422 transceivers, refer to the MAX1480 family data sheet.

Applications

Industrial Controls Level Translators **Telecommunications** Local Area Networks

Features

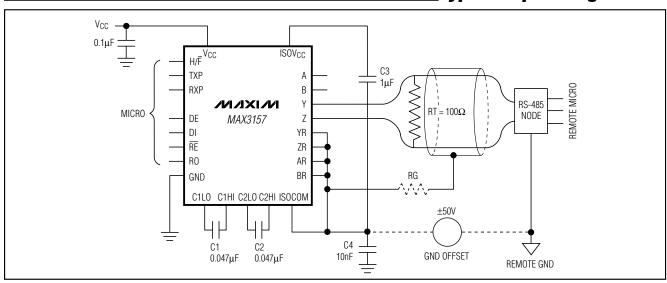
- ♦ ±50V Isolated Data Interface
- ♦ +5V Single Supply
- **♦ Low-Cost Replacement for Opto-Isolated Transceivers**
- **♦ True Fail-Safe Receiver While Maintaining** EIA/TIA-485 Compliance
- ♦ Pin-Selectable Full/Half-Duplex Operation
- ♦ Phase Controls to Correct for Twisted-Pair Reversal
- ♦ 25µA Low-Power Shutdown Mode
- ◆ Thermal Shutdown for Driver Overload Protection
- ♦ 28-Pin SSOP Package
- ♦ Slew Rate Limited Reduced EMI

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX3157CAI	0°C to +70°C	28 SSOP
MAX3157EAI	-40°C to +85°C	28 SSOP
MAX3157CPI	0°C to +70°C	28 PDIP
MAX3157EPI	-40°C to +85°C	28 PDIP

Pin Configurations appear at end of data sheet.

Typical Operating Circuit



MIXIM

Maxim Integrated Products 1

ABSOLUTE MAXIMUM RATINGS

All voltages referenced to GND unless otherwise noted.

Supply Voltage (V _{CC})	±75V
(ISOCOM)	
Digital Input, Output Voltage	
(DI, DE, RE, TXP, RXP, RO)	0.3V to (V _{CC} + 0.3V)
Digital Inputs (H/F) Relative to Cable G	Ground
(ISOCOM)	-0.3V to (ISOVCC + 0.3V)
Driver Output Voltage (Y, Z), Relative to	o Cable Ground
(ISOCOM)	8V to +12.5V
Receiver Input Voltage (A, B), Relative	to Cable Ground
(ISOCOM)	8V to +12.5V
Termination Connections (YR, ZR, AR,	BR) Relative to Cable
Ground (ISOCOM)	,
` /	

Charge-Pump Capacitance Low	
(C1LO, C2LO)	
Charge-Pump Capacitance High (C1HI, C2HI) Relative
to Cable Ground (ISOCOM)	0.3V to (ISOVCC + 0.3V)
Continuous Power Dissipation	
28-Pin SSOP (derate 10.8mW/°	
28-Pin PDIP (derate 14.3mW/°C	Cabove +70°C)1143mW
Operating Temperature Ranges	
MAX3157C_I	0°C to +70°C
MAX3157E_I	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +160°C
Lead Temperature (soldering, 10s))+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{CC} = +5V \pm 5\%, YR = ZR = AR = BR = ISOCOM, C1 = C2 = 0.047 \mu F, C3 = C4 = 1 \mu F, T_A = T_{MIN}$ to T_{MAX} , unless otherwise noted. Typical values are at $V_{CC} = +5V$ and $T_A = +25^{\circ}C$.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DRIVER	•					
Differential Driver Output, No Load	V _{OD1}	Figure 1, R = $10M\Omega$			6	٧
Differential Driver Output,	Vone	Figure 1, R = 50Ω (RS-422)	2.0			V
Loaded	V _{OD2}	Figure 1, R = 27Ω (RS-485)	1.5			V
Change in Magnitude of Differential Output Voltage (Note 2)	ΔV _{OD}	Figure 1, R = 50Ω or R = 27Ω			0.2	V
Driver Common-Mode Voltage	Voc	Figure 1, R = 50Ω or R = 27Ω , V_{OC} relative to ISOCOM			3.5	V
Change in Magnitude of Common-Mode Voltage (Note 2)	ΔV _{OC}	Figure 1, R = 50Ω or R = 27Ω			0.2	V
Input High Voltage	VIH	DE, DI, RE, TXP, RXP, relative to GND, H/F relative to ISOCOM	2.0			V
Input Low Voltage	VIL	DE, DI, RE, TXP, RXP, relative to GND, H/F relative to ISOCOM			0.8	V
DI Input Hysteresis	V _H YS			100		mV
Input Current	I _{IN1}	DE, DI, RE			±2	μΑ
	I _{IN2}	H/F, TXP, RXP internal pulldown	10		40	μΛ
January Construct (Append D)	linio	DE = GND, V_{CC} = GND or 5.25V, -7V \leq (V _{IN} - V _{ISOCOM}) \leq 12V, AR = BR = ISOCOM	-5.0		8.0	mA
Input Current (A and B)	IN3	DE = GND, V_{CC} = GND or 5.25V, -7V \leq (V_{IN} - V_{ISOCOM}) \leq 12V, AR open, BR open	-0.58	_	1.0	TINA

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC} = +5V \pm 5\%, YR = ZR = AR = BR = ISOCOM, C1 = C2 = 0.047 \mu F, C3 = C4 = 1 \mu F, T_A = T_{MIN}$ to T_{MAX} , unless otherwise noted. Typical values are at $V_{CC} = +5V$ and $T_A = +25^{\circ}C$.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Output Leakage (Y and Z)		DE = GND, V_{CC} = GND or 5.25V, -7V \leq (V _{IN} - V _{ISOCOM}) \leq 12V, YR = ZR = ISOCOM	-5.0		8.0	mA	
Full Duplex	IO	DE = GND, V_{CC} = GND or 5.25V, -7V \leq (V _{IN} - V _{ISOCOM}) \leq 12V, YR open, ZR open	-0.58		1.0	MA	
Driver Short-Circuit Output	I _{OSD1}	V _{ISOCOM} - 7V ≤ V _{OUT} ≤ V _{ISOVCC}	-250			mA	
Current (Note 3)	I _{OSD2}	VISOCOM - 7V ≤ VOUT ≤ VISOCOM +12V			250	IIIA	
RECEIVER							
Receiver Differential Threshold Voltage	V _{TH}	-7V ≤ V _{CM} - V _{ISOCOM} ≤ 12V	-200	-125	-50	mV	
Receiver Input Hysteresis	ΔV_{TH}			25		mV	
Receiver Output High Voltage	VoH	$I_O = -4mA$, $V_{ID} = -50mV$	V _{CC} - 1.5			V	
Receiver Output Low Voltage	V _{OL}	I _O = 4mA, V _{ID} = -200mV			0.4	V	
Three-State Output Current at Receiver	lozr	$0.4V \le V_{\text{O}} \le 2.4V$			±1	μΑ	
Receiver Input Resistance		-7V ≤ V _{CM} - V _{ISOCOM} ≤ 12V, AR = BR = ISOCOM	1.5			1.0	
	R _{IN}	-7V ≤ V _{CM} - V _{ISOCOM} ≤ 12V, AR open, BR open	12			kΩ	
Receiver Output Short-Circuit Current	Iosr	0V ≤ V _{RO} ≤ V _{CC}	±7		±100	mA	
SUPPLY CURRENT							
Supply Current	Icc	No load, $\overline{RE} = DE = DI = GND$ or V_{CC}		25	85	mA	
Supply Current in Shutdown	1	DE = GND, RE = VCC, VISOCOM = GND		25	60		
Mode	ISHDN	DE = GND, RE = VCC, VISOCOM = ±50V			±600	μA	
Maximum Ground Differential	ΔV _{GND}	DE = GND, RE = V _{CC} , I ISOCOM leakage I ≤ 600µA	±50			V	

SWITCHING CHARACTERISTICS

 $(V_{CC} = +5V \pm 5\%, YR = ZR = AR = BR = ISOCOM, C1 = C2 = 0.047 \mu F, C3 = C4 = 1 \mu F.$ Typical values are at $V_{CC} = +5V$ and $T_A = +25$ °C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Driver Input to Output	tDPLH	Figures 3 and 5, $R_{DIFF} = 54\Omega$, $C_{L1} = C_{L2} = 100pF$	400	1400	2000	20
	tDPHL	Figures 3 and 5, R _{DIFF} = 54Ω , C _{L1} = C _{L2} = 100 pF	400	1400	2000	ns
Driver Output Skew tpplh - tpphl	tDSKEW	Figures 3 and 5, R _{DIFF} = 54Ω , C _{L1} = C _{L2} = 100 pF			250	ns
Driver Rise or Fall Time	t _{DR} , t _{DF}	Figures 3 and 5, R _{DIFF} = 54Ω , C _{L1} = C _{L2} = 100 pF	200	1200	2200	ns
Maximum Data Rate	f _{MAX}		250			kbps

SWITCHING CHARACTERISTICS (continued)

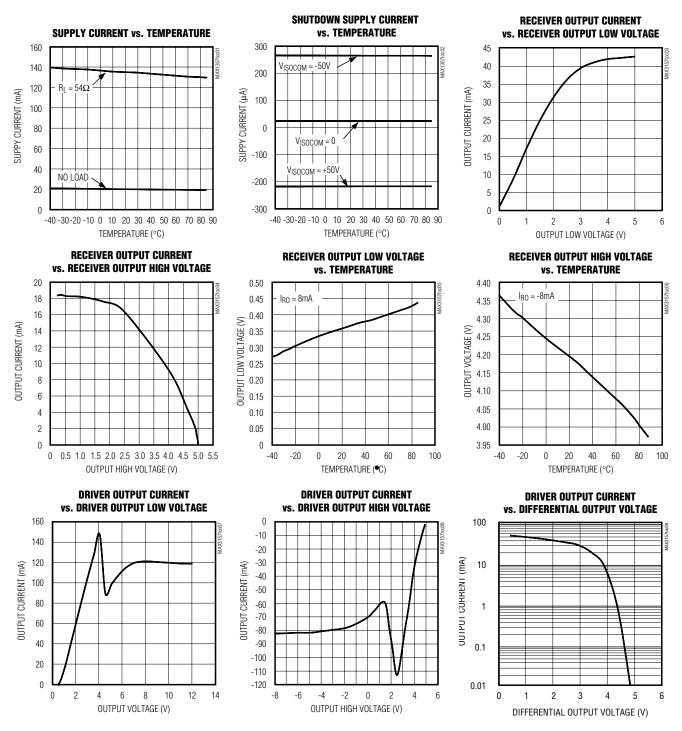
 $(V_{CC} = +5V \pm 5\%, YR = ZR = AR = BR = ISOCOM, C1 = C2 = 0.047 \mu F, C3 = C4 = 1 \mu F.$ Typical values are at $V_{CC} = +5V$ and $T_{A} = +25$ °C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Driver Enable to Output High t _{DZH} F		Figures 4 and 6, C _L = 100pF, S2 closed			3500	ns
Driver Enable to Output Low	t _{DZL}	Figures 4 and 6, C _L = 100pF, S1 closed			3500	ns
Driver Disable Time from Low	t _{DLZ}	Figures 4 and 6, C _L = 15pF, S1 closed			300	ns
Driver Disable Time from High	tDHZ	Figures 4 and 6, C _L = 15pF, S2 closed			300	ns
Receiver Input to Output	tRPLH, tRPHL	Figures 7 and 9; V _{ID} ≥ 2.0V		440	600	ns
Differential Receiver Skew trplh - trphl	trskew	Figures 7 and 9; V _{ID} ≥ 2.0V		20		ns
Receiver Enable to Output Low	t _{RZL}	Figures 2 and 8, C _L = 100pF, S1 closed		20	50	ns
Receiver Enable to Output High	trzh	Figures 2 and 8, C _L = 100pF, S2 closed		20	50	ns
Receiver Disable Time from Low	t _{RLZ}	Figures 2 and 8, C _L = 100pF, S1 closed		200	500	ns
Receiver Disable Time from High	t _{RHZ}	Figures 2 and 8, C _L = 100pF, S2 closed		200	500	ns
Time to Shutdown	tshdn	(Note 4)	50	200	700	ns
Driver Enable from Shutdown to Output High	t _{DZH} (SHDN)	Figures 4 and 6, C _L = 15pF, S2 closed		0.2	1	ms
Driver Enable from Shutdown to Output Low	t _{DZL} (SHDN)	Figures 4 and 6, C _L = 15pF, S1 closed		0.2	1	ms
Receiver Enable from Shutdown t _{RZH} to Output High (SHDI		Figures 2 and 8, C _L = 100pF, S2 closed		0.2	1	ms
Receiver Enable from Shutdown tRZL (SHDN) Figure		Figures 2 and 8, C _L = 100pF, S1 closed		0.2	1	ms
Charge-Pump Oscillating fosc				1.3		MHz

- **Note 1:** All currents into the device are positive; all currents out of the device are negative. All voltages are referred to device ground unless otherwise noted.
- Note 2: ΔV_{OD} and ΔV_{OC} are the changes in V_{OD} and V_{OC} , respectively, when the DI input changes state.
- Note 3: Current level applies to peak current just prior to foldback-current limiting.
- Note 4: The device is put into shutdown by bringing RE high and DE low. If the enable inputs are in this state for less than 50ns, the device is guaranteed not to enter shutdown. If the enable inputs are in this state for at least 700ns, the device is guaranteed to have entered shutdown.

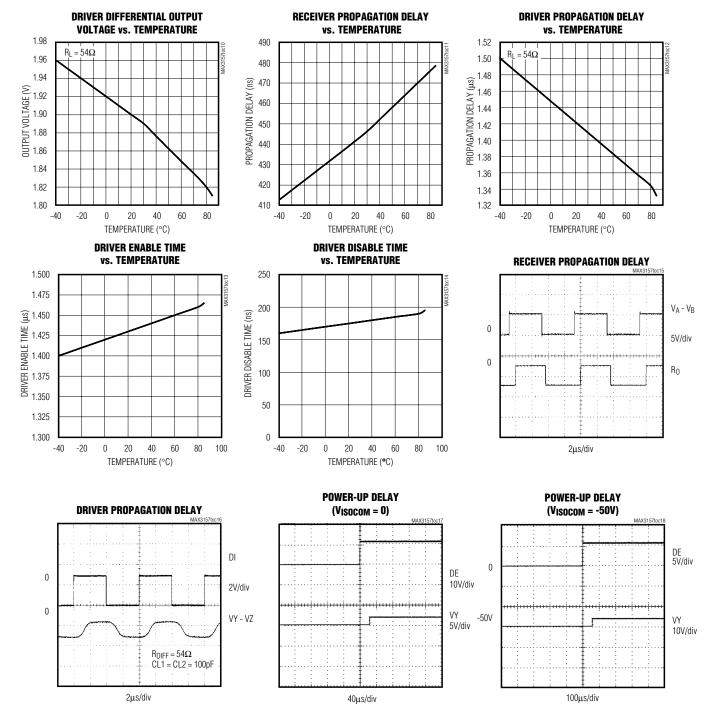
Typical Operating Characteristics

 $(V_{CC} = +5V, YR = ZR = AR = BR = ISOCOM, C1 = C2 = 0.047 \mu F, C3 = 1 \mu F, C4 = 10 n F, T_A = +25 ° C, unless otherwise noted.)$



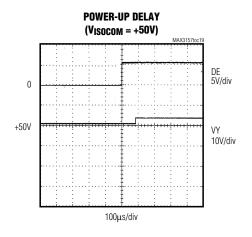
Typical Operating Characteristics (continued)

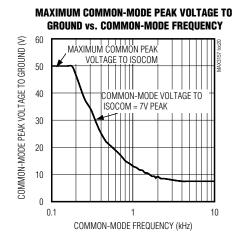
 $(V_{CC} = +5V, YR = ZR = AR = BR = ISOCOM, C1 = C2 = 0.047 \mu F, C3 = 1 \mu F, C4 = 10 n F, T_A = +25 ° C, unless otherwise noted.)$



Typical Operating Characteristics (continued)

 $(V_{CC} = +5V, YR = ZR = AR = BR = ISOCOM, C1 = C2 = 0.047 \mu F, C3 = 1 \mu F, C4 = 10 n F, T_A = +25 ^{\circ}C, unless otherwise noted.)$





Test Circuits and Timing Diagrams

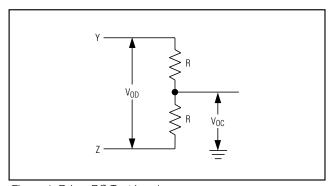


Figure 1. Driver DC Test Load

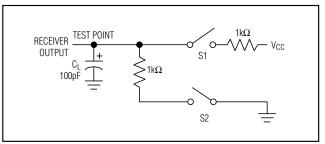


Figure 2. Receiver Enable/Disable Timing Test Load

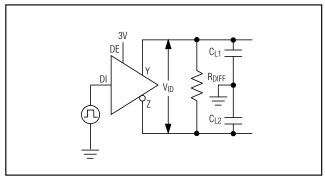


Figure 3. Driver Timing Test Circuit

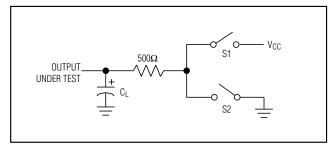


Figure 4. Driver Enable/Disable Timing Test Load

Test Circuits and Timing Diagrams (continued)

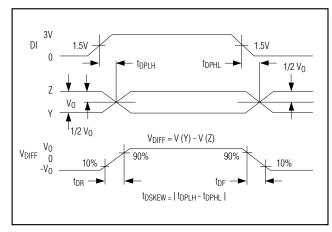


Figure 5. Driver Propagation Delays

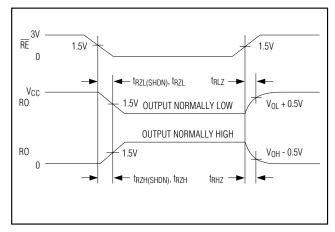


Figure 8. Receiver Enable and Disable Times

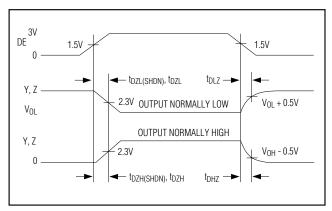


Figure 6. Driver Enable and Disable Times

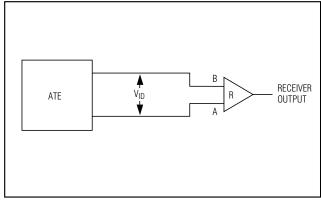


Figure 9. Receiver Propagation Delay Test Circuit

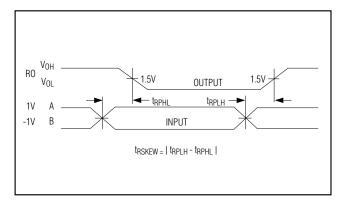


Figure 7. Receiver Propagation Delays

Pin Description

PIN							
SSOP	PDIP	NAME	FUNCTION				
1	2	RO	Receiver Output. When \overline{RE} is low and A - B > -50mV, RO will be high; if A - B \leq -200mV, RO will be low.				
2	3	RE	Receiver Output Enable. Drive RE low to enable RO.				
3	4	DE	Driver Output Enable. Drive DE high to enable driver outputs.				
4	5	DI	Driver Input. With DE high, a low on DI forces the noninverting output low and the inverting output high; with DI high, outputs reverse.				
5	6	C1LO	Connect to the negative terminal of C1 (0.047µF, 50V).				
6, 7, 12, 22, 23	1, 12, 14, 15, 28	N.C.	No Connection				
8	7	C1HI	Connect to the positive terminal of C1 (0.047µF, 50V).				
9	8	ISOVCC	Internally generated power-supply voltage, referenced to the cable ground (ISOCOM). Connect a 1µF capacitor to ISOCOM.				
10	9	H/F	Half/Full-Duplex Selector Pin. Leave open, or connect to ISOCOM to select Full Duplex, or connect to ISOVCC to select Half Duplex.				
11	10	ZR	Connect to ISOCOM for a 1.5k Ω input impedance on Z. Leave open for a 12k Ω input impedance on Z.				
13	11	YR	Connect to ISOCOM for a 1.5k Ω input impedance on Y. Leave open for a 12k Ω input impedance on Y.				
14	13	ISOCOM	Cable Ground				
15	16	Z	Inverting Driver Output (and Inverting Receiver Input in Half-Duplex Mode)				
16	17	Y	Noninverting Driver Output (and Noninverting Receiver Input in Half-Duplex Mode)				
17	18	В	Inverting Receiver Input in Full-Duplex Mode				
18	19	А	Noninverting Receiver Input in Full-Duplex Mode				
19	20	BR	Connect to ISOCOM for a 1.5k Ω input impedance on B. Leave open for a 12k Ω input impedance on B.				
20	21	AR	Connect to ISOCOM for a 1.5k Ω input impedance on A. Leave open for a 12k Ω input impedance on A.				
21	22	C2HI	Connect to the positive terminal of C2 (0.047µF, 50V).				
24	23	C2LO	Connect to the negative terminal of C2 (0.047µF, 50V).				
25	24	GND	Ground				
26	25	TXP	Transmitter Phase. Leave open, or connect to GND for normal transmitter polarity, or connect to V _{CC} to invert the transmitter polarity.				
27	26	RXP	Receiver Phase. Leave open, or connect to GND for normal receiver polarity, or connect to V _{CC} to invert the receiver polarity.				
28	27	Vcc	+4.75V to +5.25V Positive Supply. Connect a 0.1µF capacitor to GND.				



Detailed Description

The MAX3157 is a high CMRR RS-485/RS-422 data-communications interface providing $\pm 50V$ isolation in a hybrid microcircuit. A single $\pm 5V$ supply on the logic side powers both sides of the interface, with external $\pm 50V$ capacitors transferring power from the logic side to the isolated side (see *Block Diagram*). The MAX3157 typically draws 25mA of supply current when unloaded or when fully loaded with the driver disabled. Supply current drops to $25\mu A$ when the device is placed in shutdown mode (see *Low-Power Shutdown Mode*). The MAX3157 transceiver for RS-485/RS-422 communication contains one driver and one receiver.

This device features fail-safe circuitry, which guarantees a logic-high receiver output when the receiver inputs are open or shorted, or when they are connected to a terminated transmission line with all drivers disabled (see *Fail-Safe*). The MAX3157 is selectable between half- and full-duplex communication by connecting a selector pin to ISOVCC or ISOCOM, respectively. Drivers are output short-circuit current limited. Thermal shutdown circuitry protects drivers against excessive power dissipation. When activated, the thermal shutdown circuitry places the driver outputs into a high-impedance state. The device also features independently programmable receiver and transmitter output phase via separate pins. The MAX3157 is a low-cost replacement for opto-isolated transceivers.

Fail-Safe

The MAX3157 **guarantees** a logic-high receiver output when the receiver inputs are shorted or open, or when they are connected to a terminated transmission line with all drivers disabled. The receiver threshold is fixed between -50mV and -200mV. If the differential receiver input voltage (A - B) is greater than or equal to -50mV, RO is logic high. If A - B is less than or equal to -200mV, RO is logic low. In the case of a terminated bus with all transmitters disabled, the receiver's differential input voltage is pulled to 0V by the termination. With the receiver thresholds of the MAX3157, this results in a logic high with a 50mV minimum noise margin. Unlike competitor's fail-safe devices, the -50mV to -200mV threshold complies with the ±200mV EIA/TIA-485 standard.

MAX3157 Programming

The MAX3157 has several programmable operating modes. Occasionally, twisted-pair lines are reversed. The MAX3157 has two pins that invert the phase of the driver and the receiver to correct for this problem. For normal operation, drive TXP and RXP low, connect them to ground, or leave them unconnected (internal pulldown). To invert the driver phase, drive TXP high or

connect it to V_{CC}. To invert the receiver phase, drive RXP high or connect it to V_{CC}. Note that the receiver threshold is positive when RXP is high.

The MAX3157 can operate in full- or half-duplex mode. Connect H/ \overline{F} to ISOCOM for full-duplex mode, and connect it to ISOVCC for half-duplex operation. In half-duplex mode, the receiver inputs are switched to the driver outputs, connecting outputs Y and Z to inputs A and B, respectively. In half-duplex mode, the internal full-duplex receiver input resistors are still connected to pins A and B.

Low-Power Shutdown Mode

The low-power shutdown mode is initiated by bringing both RE high and DE low. In shutdown, this device typically draws only 25µA of supply current, and no power is transferred across the isolation capacitors in this mode. RE and DE may be driven simultaneously; the parts are guaranteed not to enter shutdown if RE is high and DE is low for less than 50ns. If the inputs are in this state for at least 700ns, the parts are guaranteed to enter shutdown. Enable times t_ZH and t_ZL in the Switching Characteristics table assume the part was not in a low-power shutdown state. Enable times t_zh(SHDN) and t_zl(SHDN) assume the parts were shut down. It takes drivers and receivers longer to become enabled from low-power shutdown mode t_ZH(SHDN), t_ZL(SHDN) than from driver/receiver-disable mode $(t_ZH, t_ZL).$

Driver Output Protection

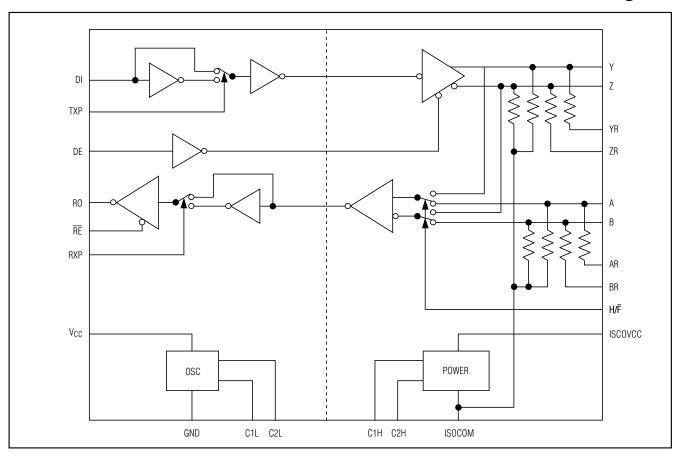
Two mechanisms prevent excessive output current and power dissipation caused by faults or by bus contention. The first, a foldback current limit on the output stage, provides immediate protection against short circuits over the whole common-mode voltage range (see *Typical Operating Characteristics*). The second, a thermal shutdown circuit, forces the driver outputs into a high-impedance state if the die temperature becomes excessive—typically around +150°C.

Applications Information

Capacitor and Grounding Resistor Selection

The value for the charge-pump capacitors C1 and C2 should be between 47nF and 100nF. Smaller values will result in insufficient supply voltage on the isolated side. Larger values are allowed but will not result in better charge-pump capacity. The values for C1 and C2, as well as that of C4, determine the maximum frequency and amplitude of the voltage difference (under operating conditions) between the local and isolated ground. Besides the capacitor values, this maximum frequency

Block Diagram



and amplitude are also determined by the resistance between the remote ground and the ISOCOM pin. The receiver input resistors will cause the isolated common voltage to go to the mean voltage of the receiver inputs, which will be a direct function of the remote ground potential. The receiver input resistance and the capacitors C1, C2, and C4 set up a time constant that limits how fast the ISOCOM pin can follow variations in the remote ground voltage. Connecting YR and ZR in halfduplex operation, or AR and BR in full-duplex operation, to ISOCOM results in a relatively low input impedance of the MAX3157 receiver inputs ($2k\Omega$). This allows for a 60Hz sine wave with a 50V maximum amplitude (see Typical Operating Characteristics). If YR, ZR, AR, and BR are left open, the receiver input impedance is $12k\Omega$ allowing up to 32 transceivers on the bus. To guarantee the same low time constant under those conditions, use a shielded cable with a $1k\Omega$ resistor connected between the shield and ISOCOM. Using a lower value for this resistor is not recommended because this could trigger

a holding current in the internal ESD protection device if the ±75V isolation limit is exceeded.

A single point hard-ground connection for the shield is recommended.

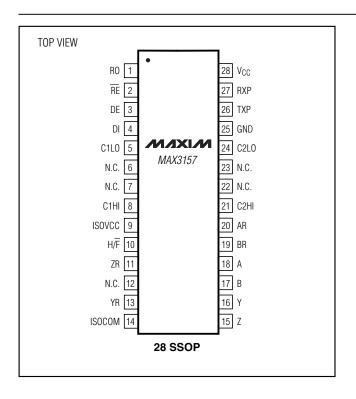
Communication Between Two MAX3157s

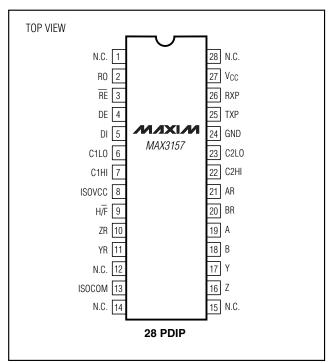
If two MAX3157 devices are used to communicate with each other, one of the devices must have ISOCOM connected to local ground. Failure to do so will result in floating ISOCOM pins, with both devices trying to adapt to the isolated ground of the other.

Chip Information

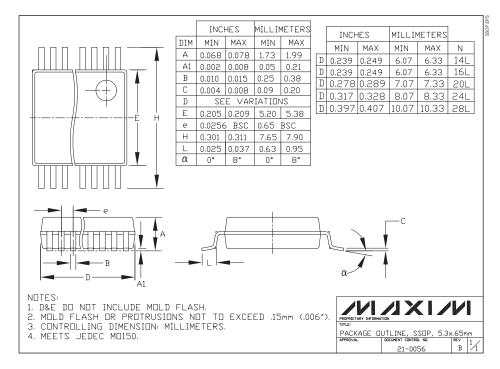
TRANSISTOR COUNT: 1309

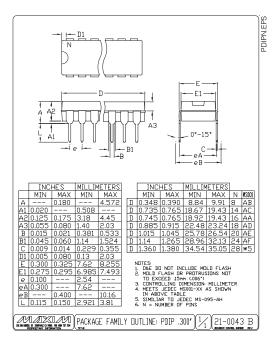
Pin Configurations





Package Information





Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.